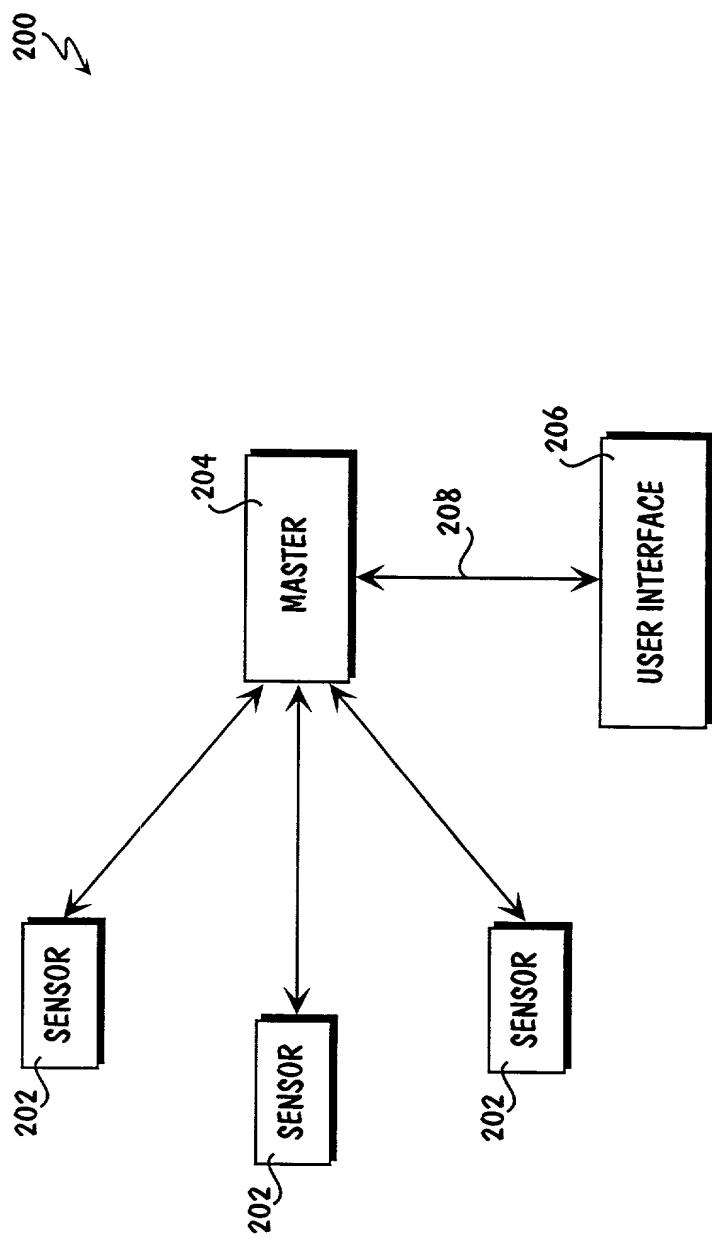


Fig. 1 (Prior Art)

FIG. 2 (Prior Art)



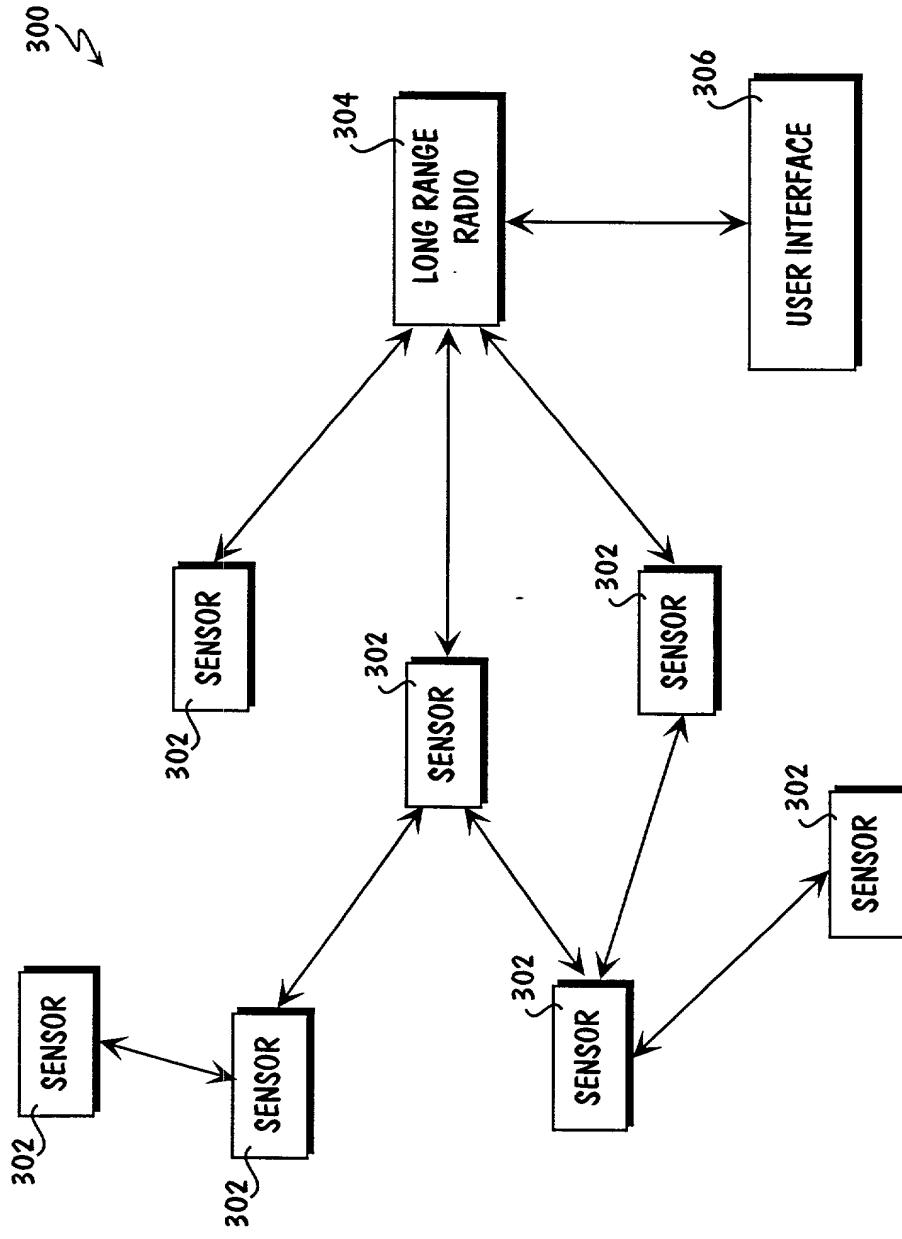


FIG. 3 (Prior Art)

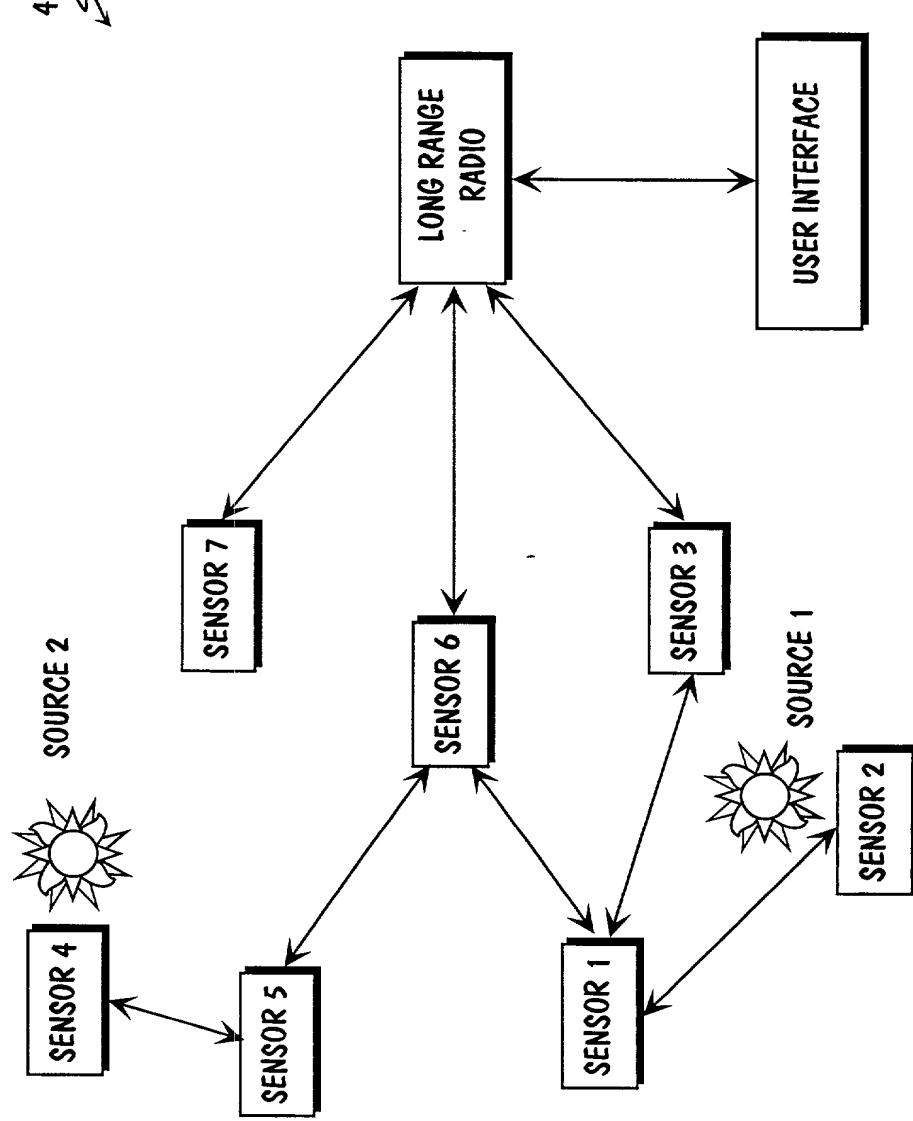


FIG. 4 (Prior Art)

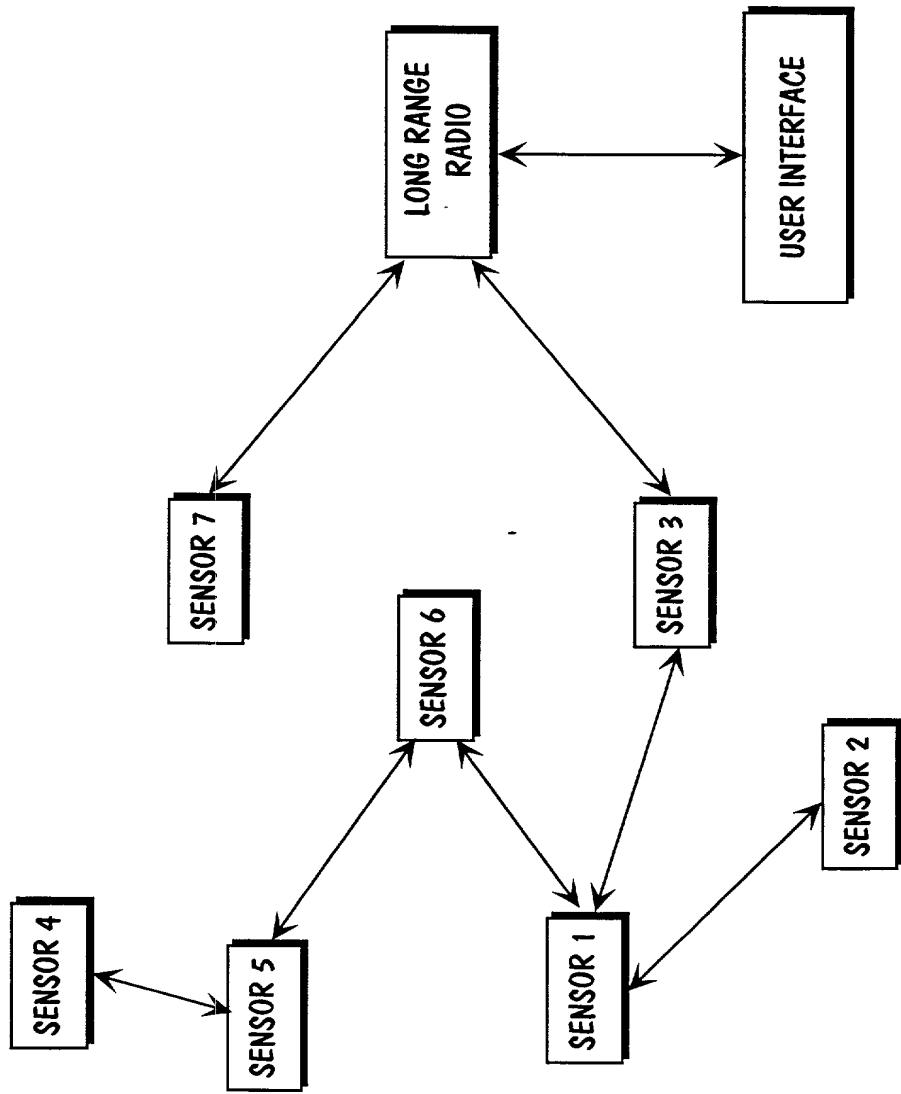


FIG. 5 (Prior Art)

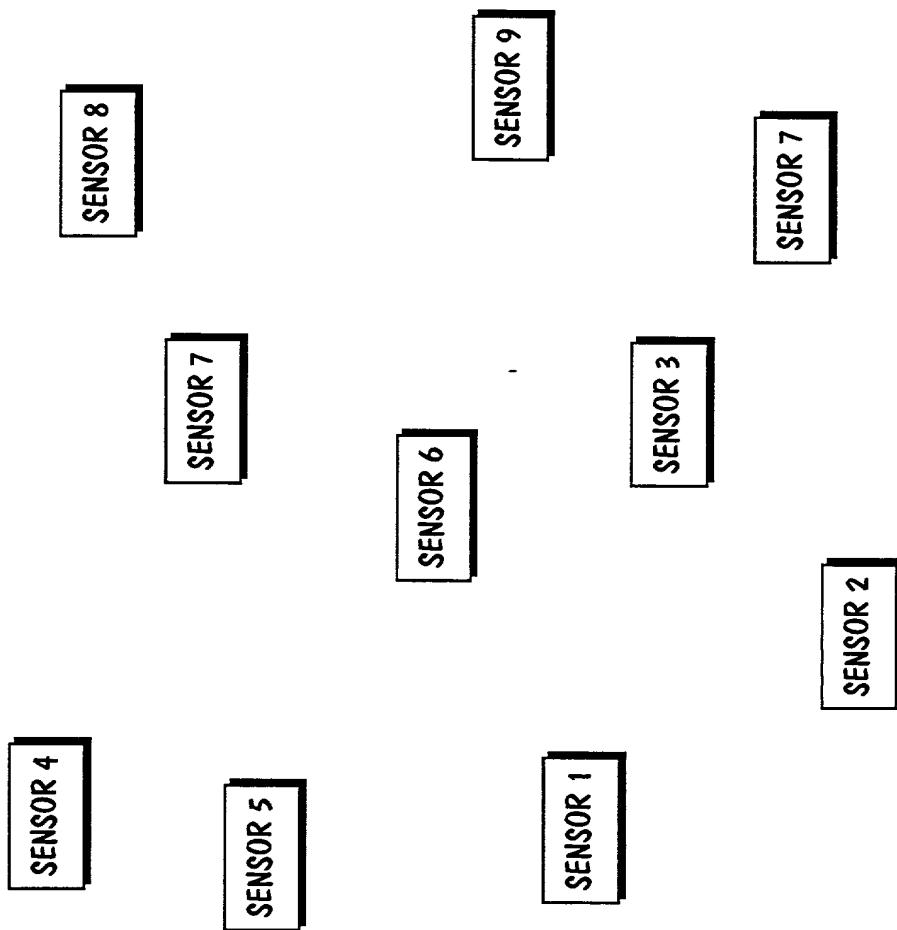


FIG. 6 (Prior Art)

FIG. 7 (Prior Art)

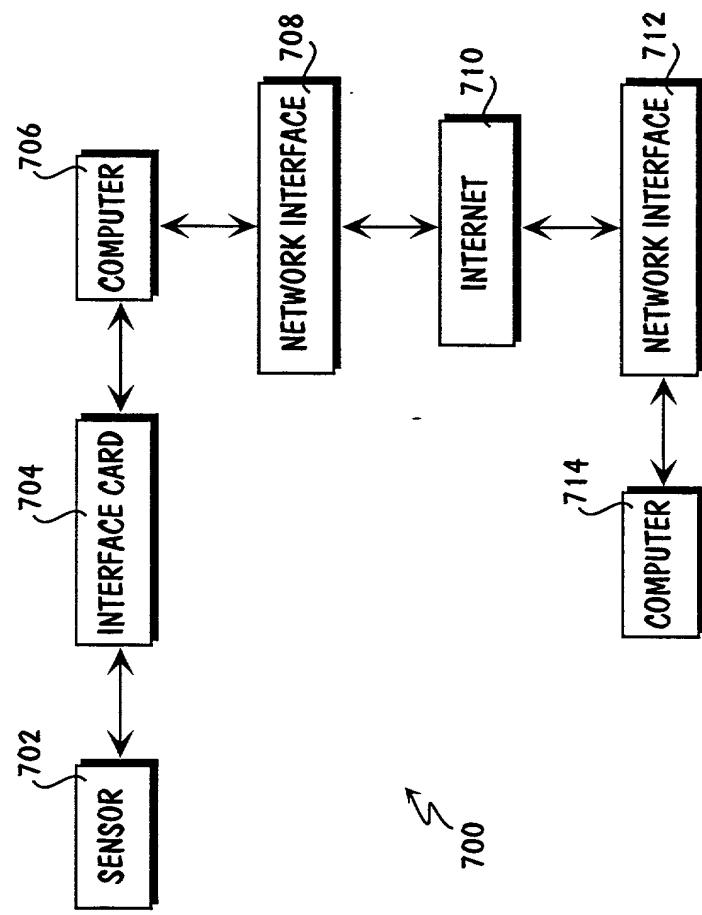
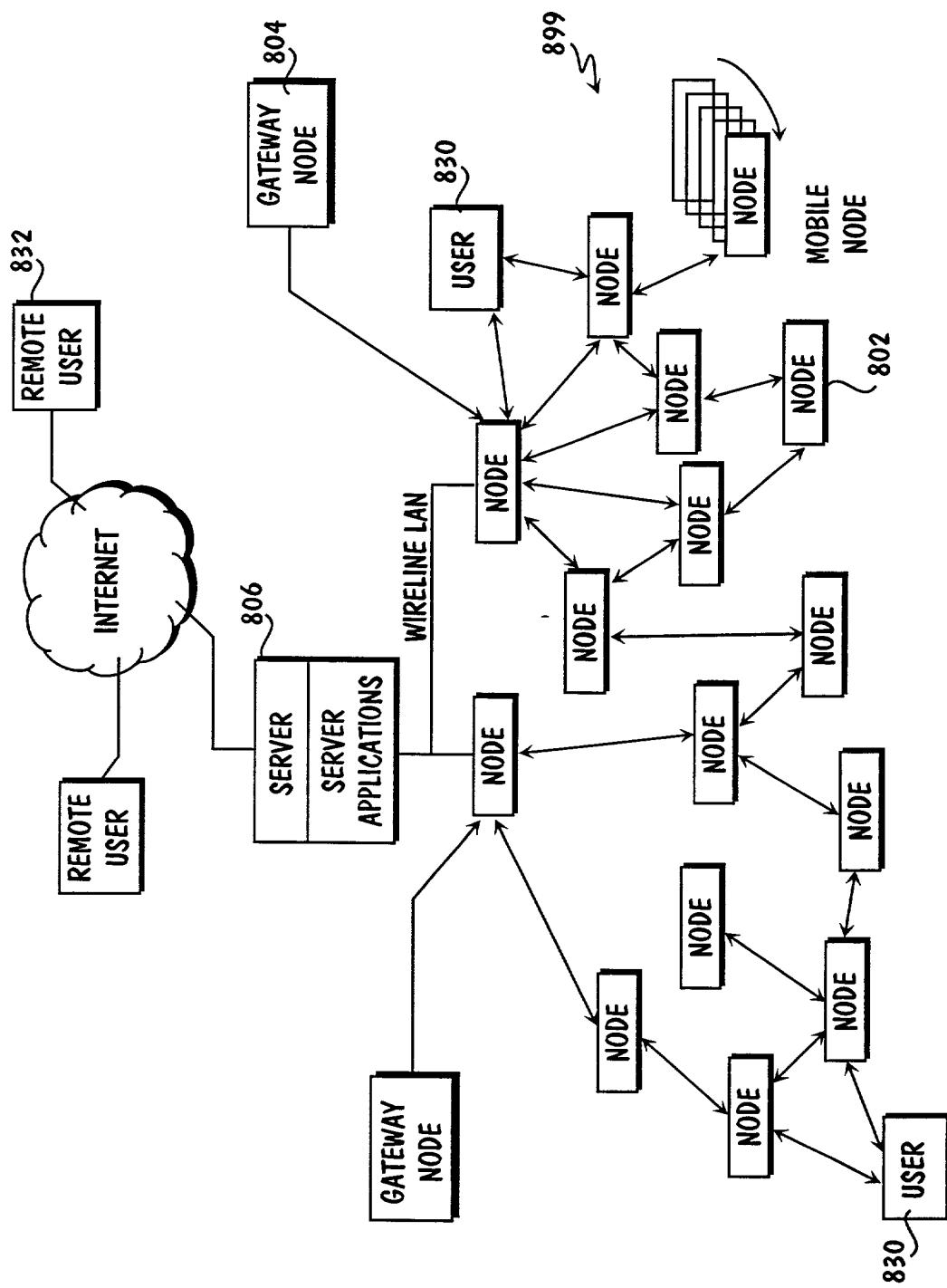


FIG. 8



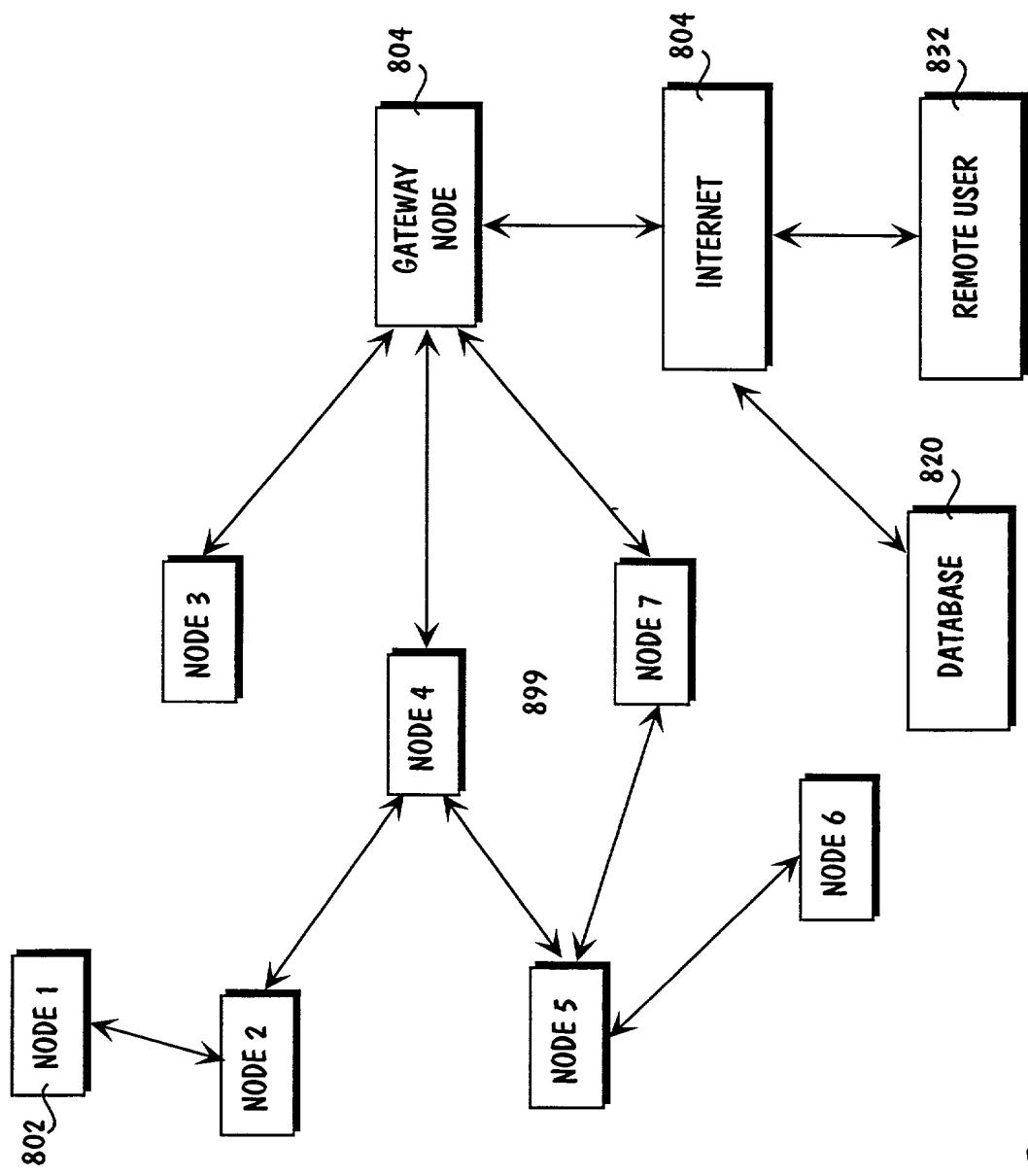


FIG. 9

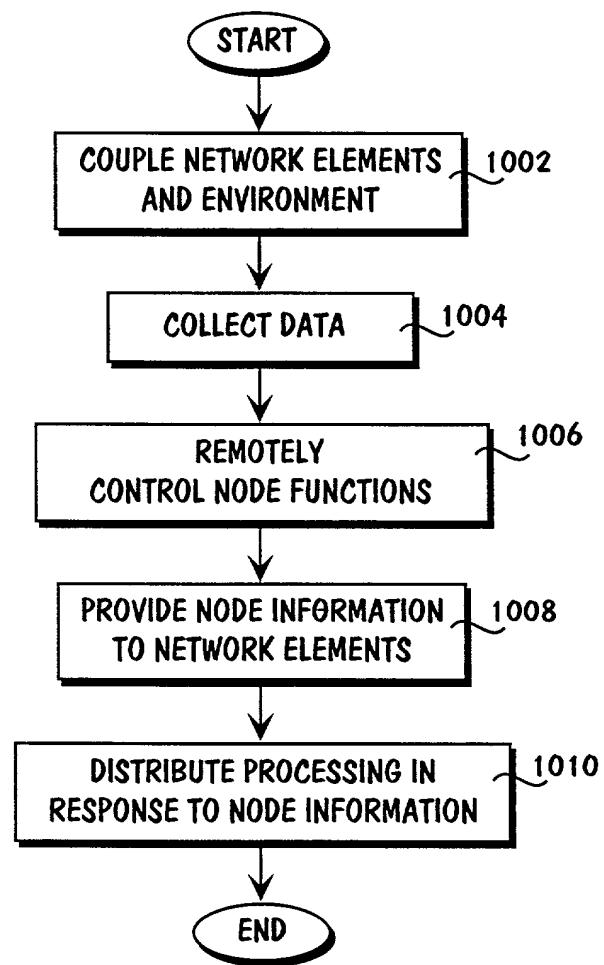
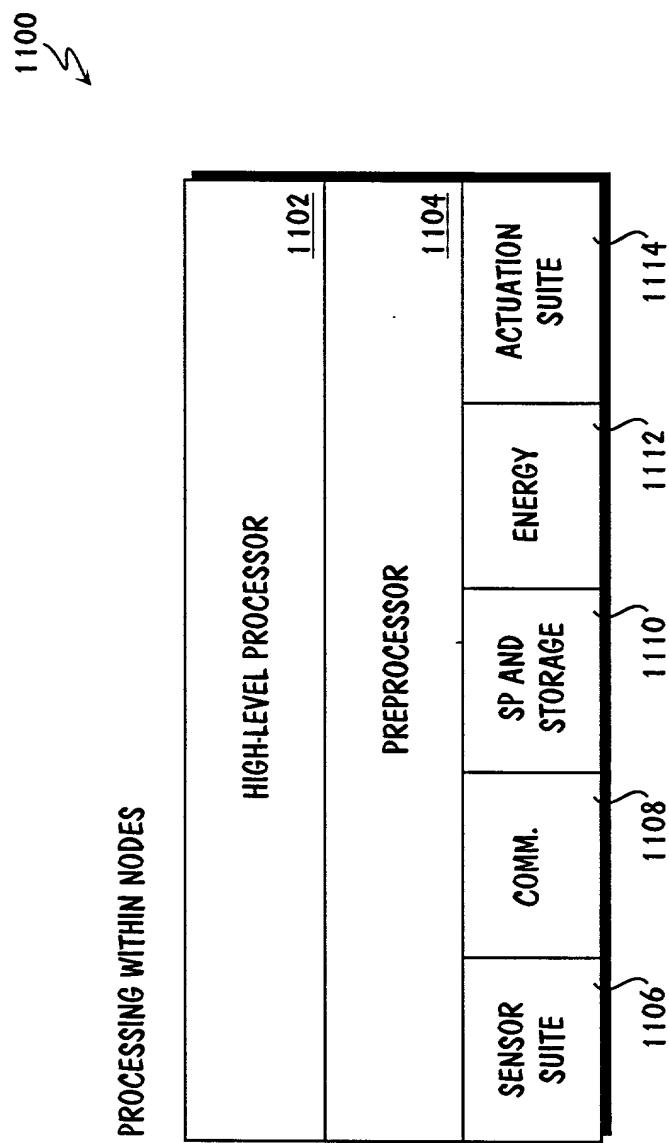


FIG. 10

==  
E|G.



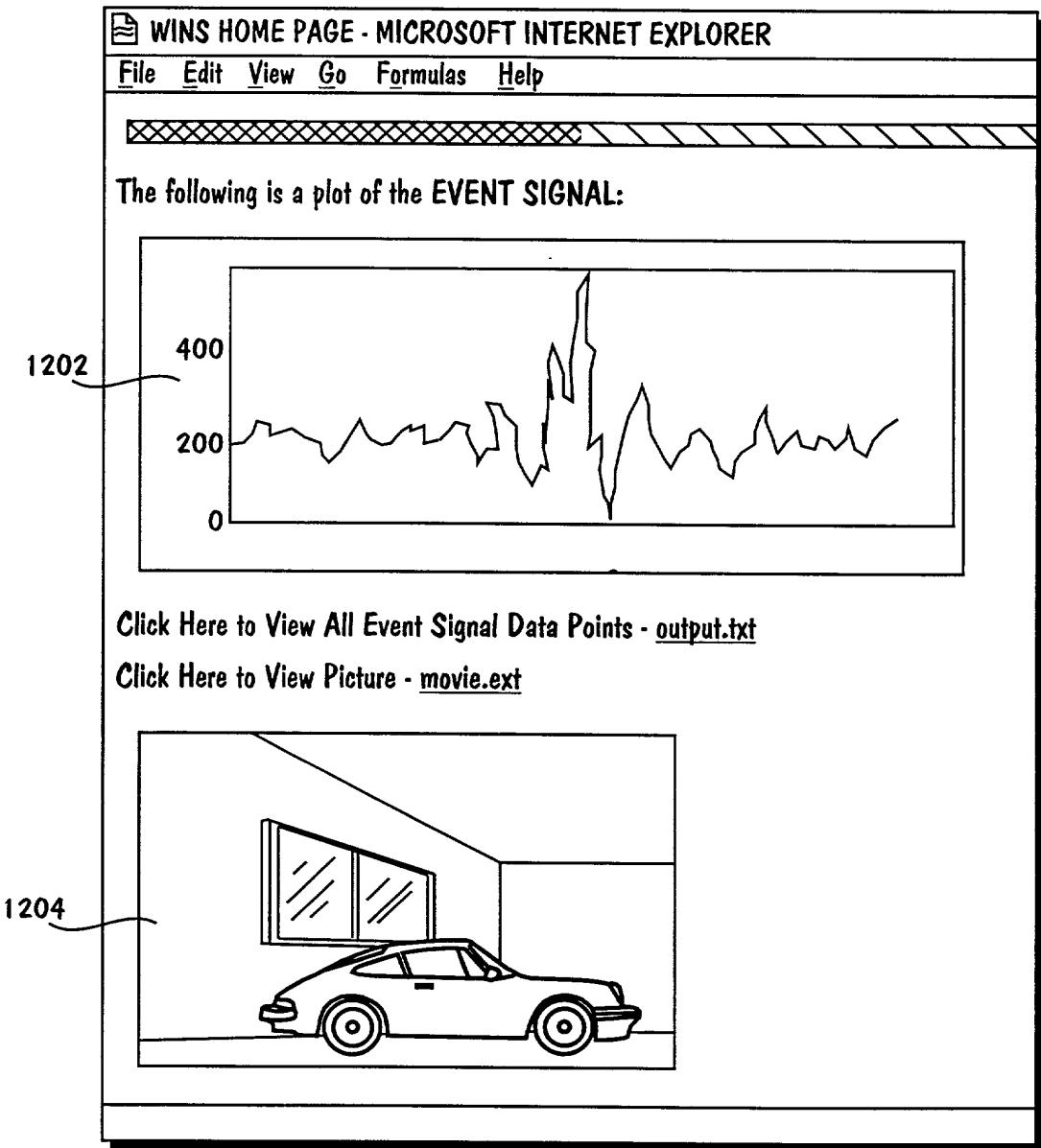


FIG. 12

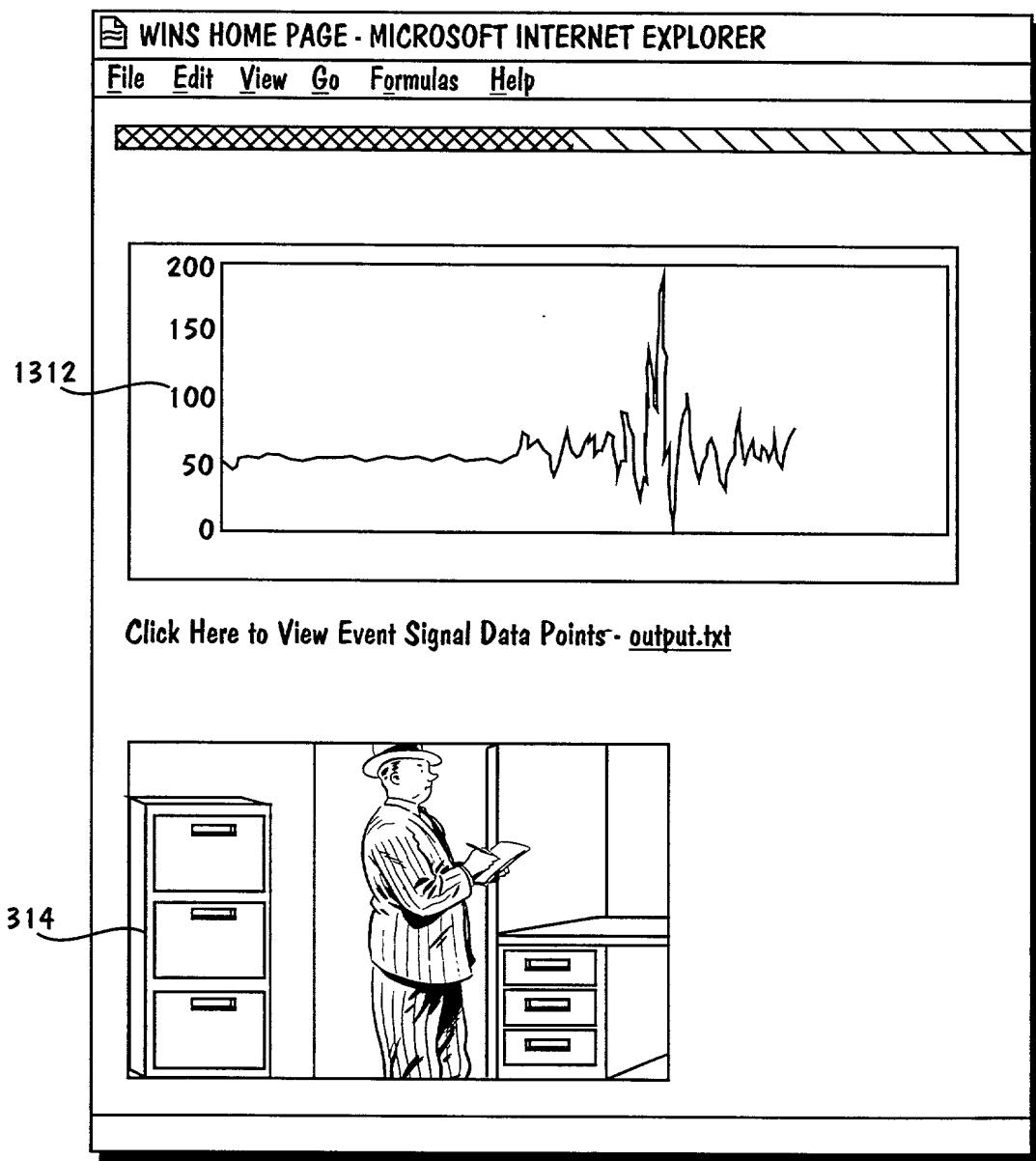
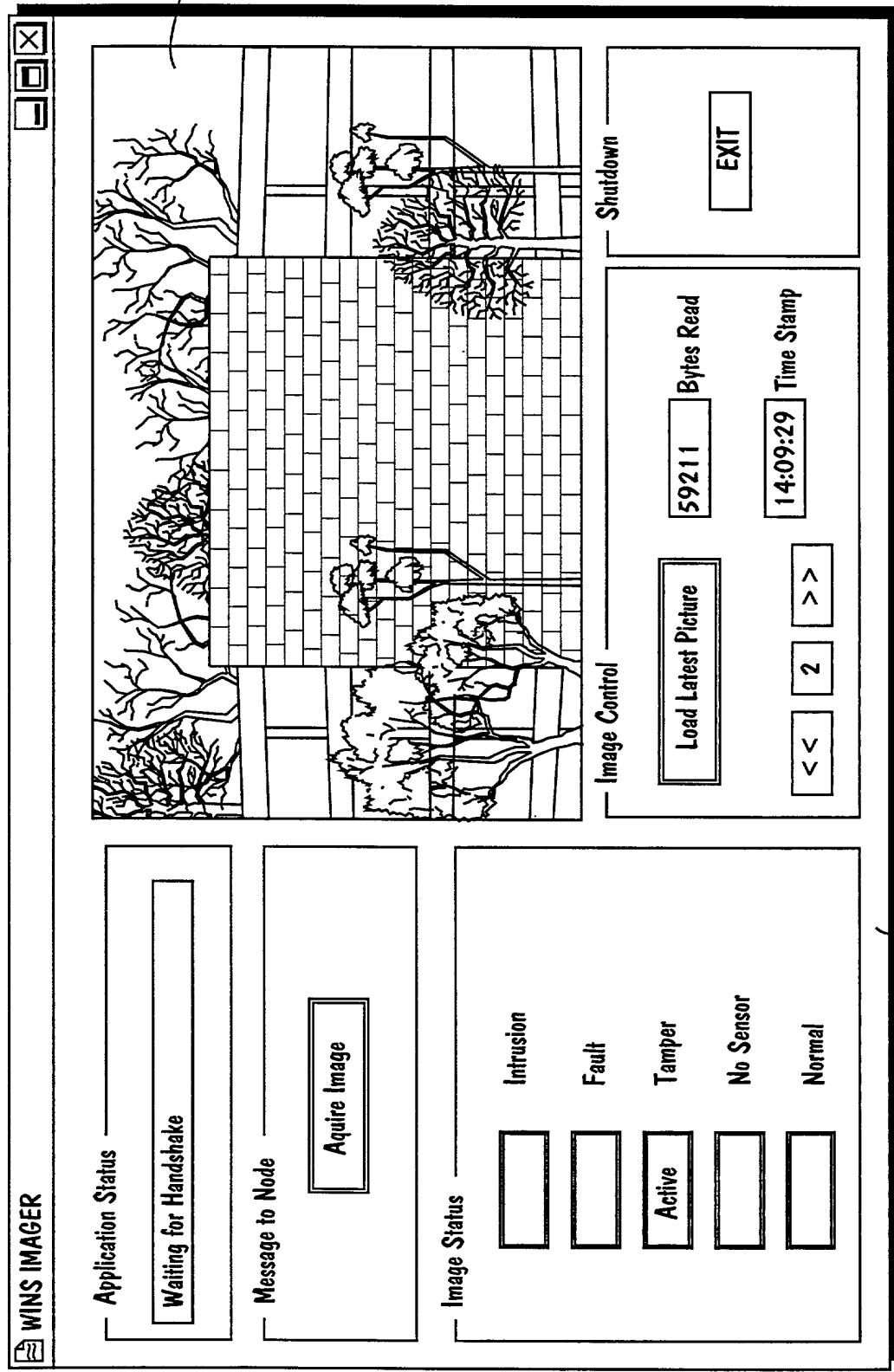


FIG. 13

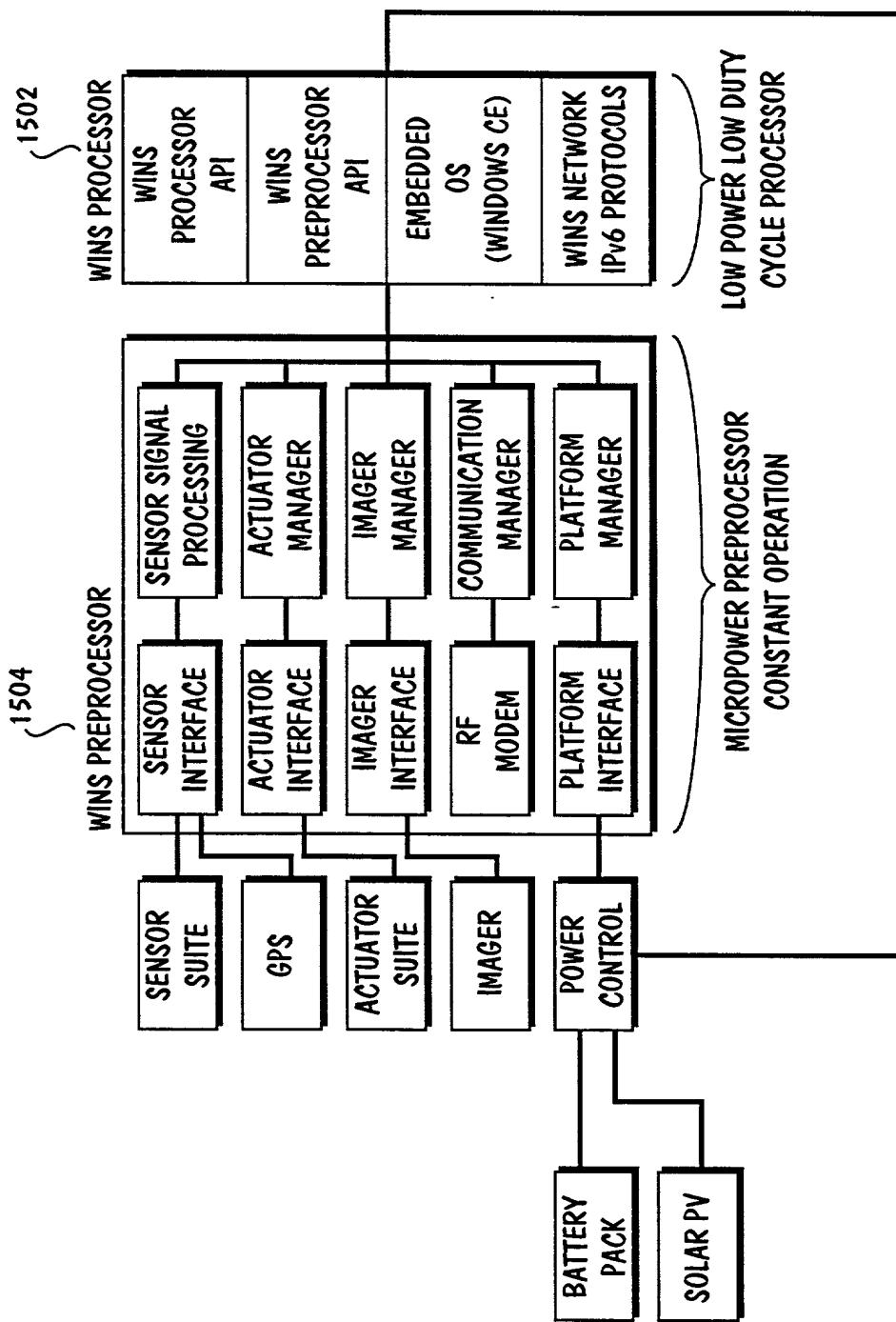
1400

1402



1404

FIG. 14



15  
FIG.

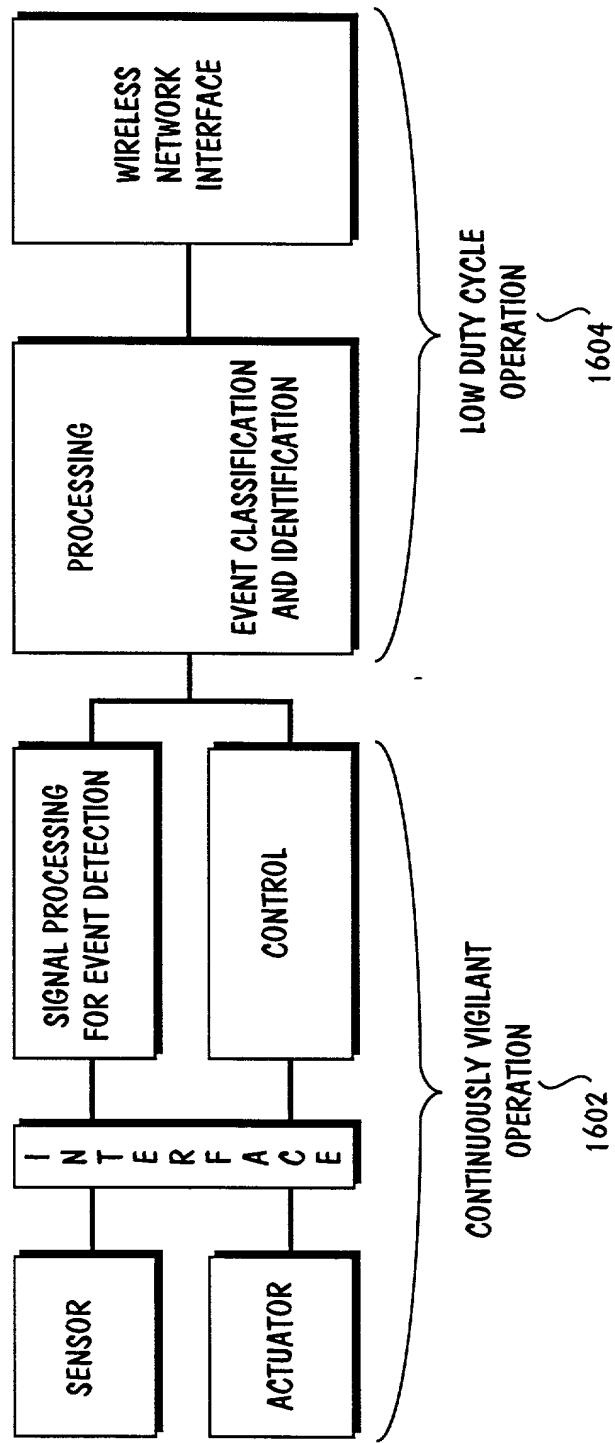


FIG. 16

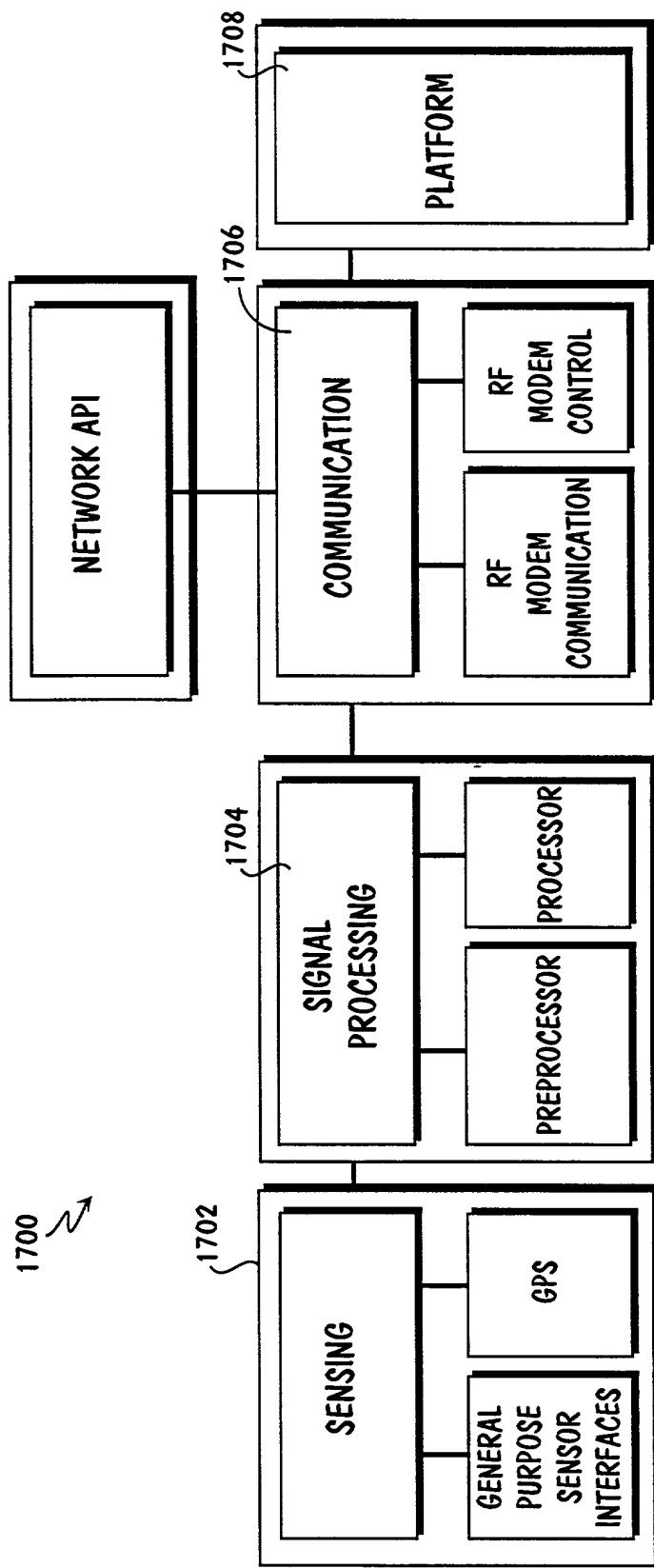


FIG. 17

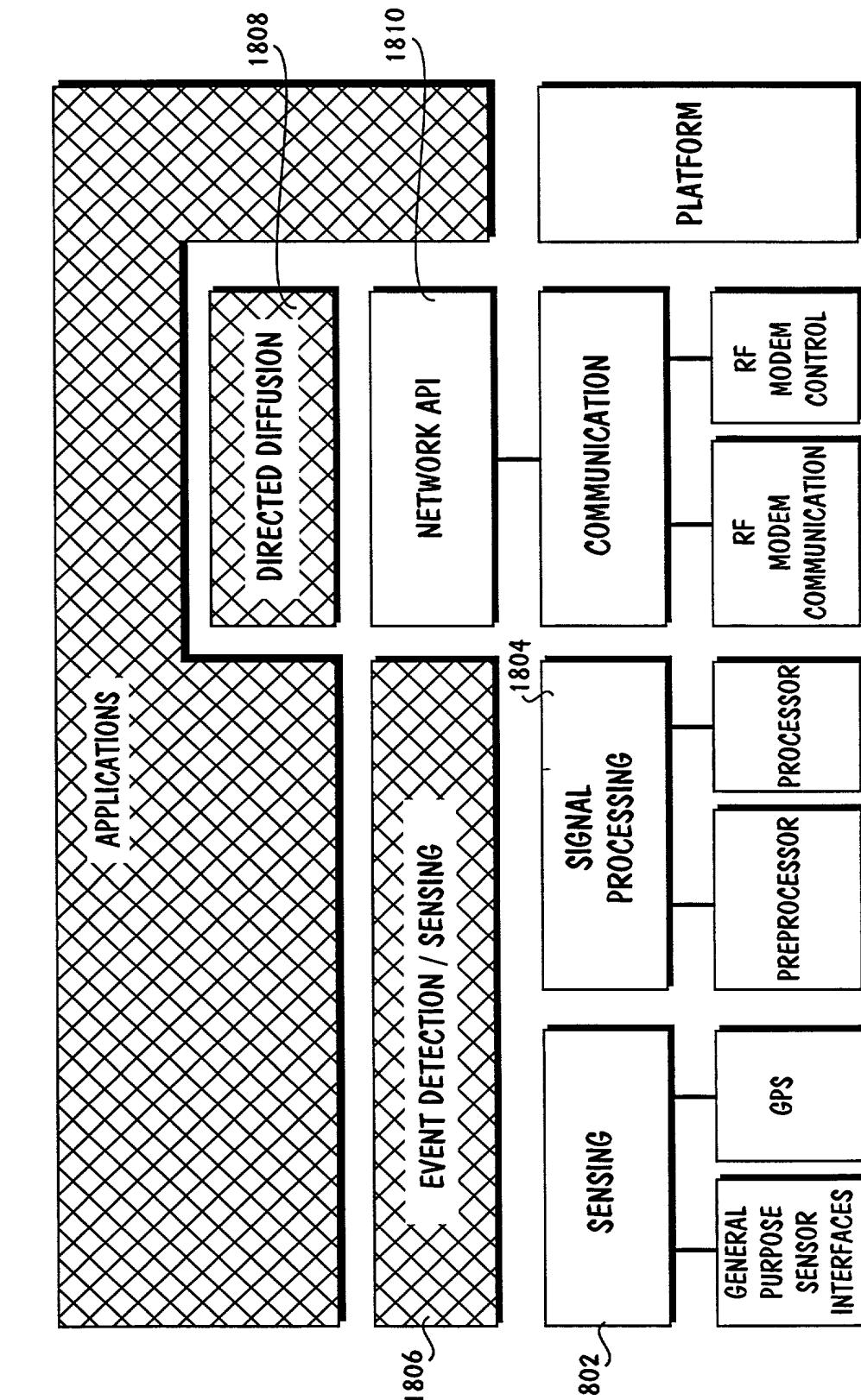


FIG. 18

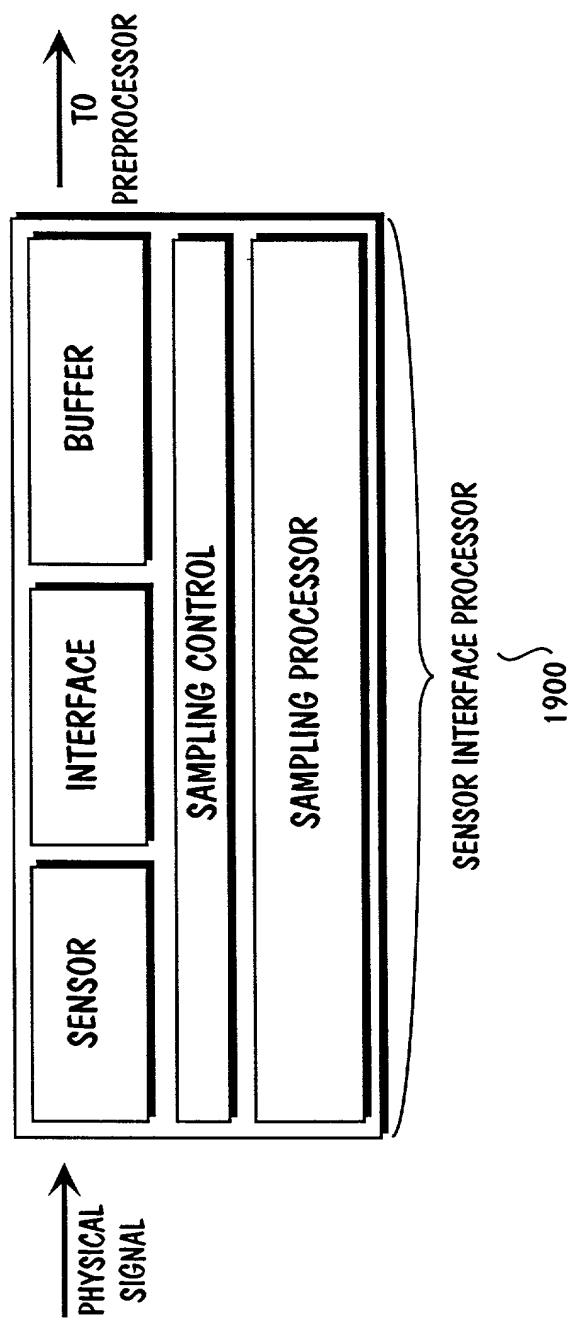


Fig. 19

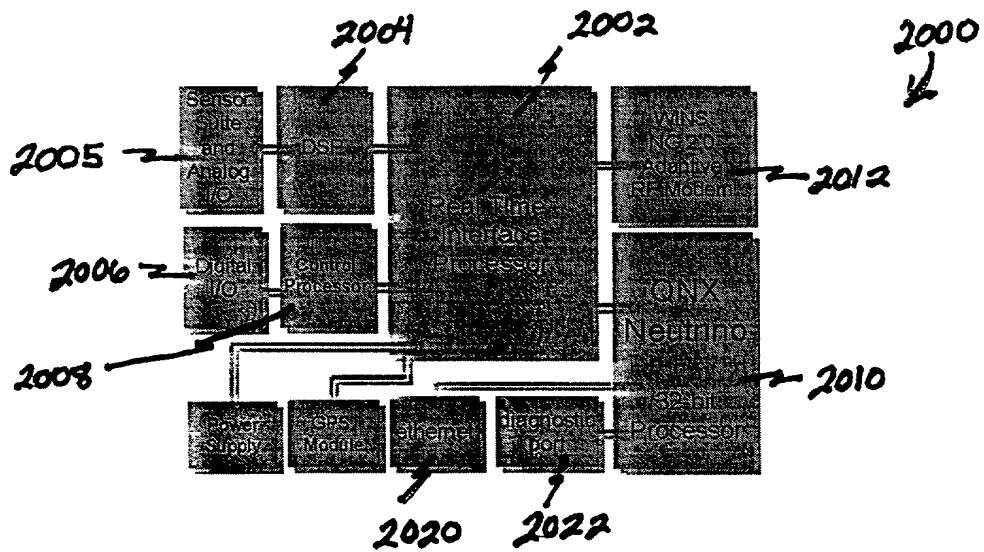
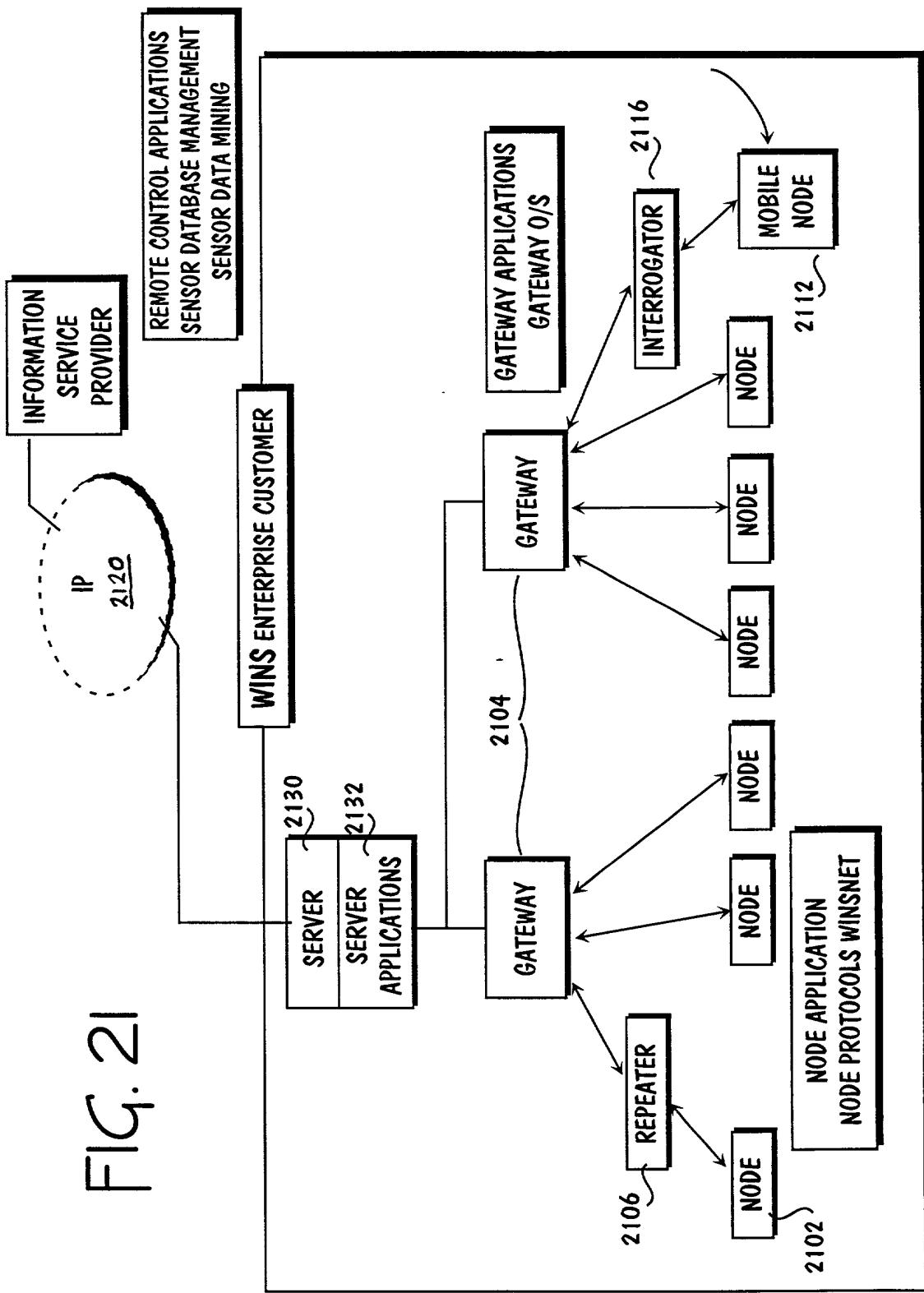


FIGURE 20

FIG. 21



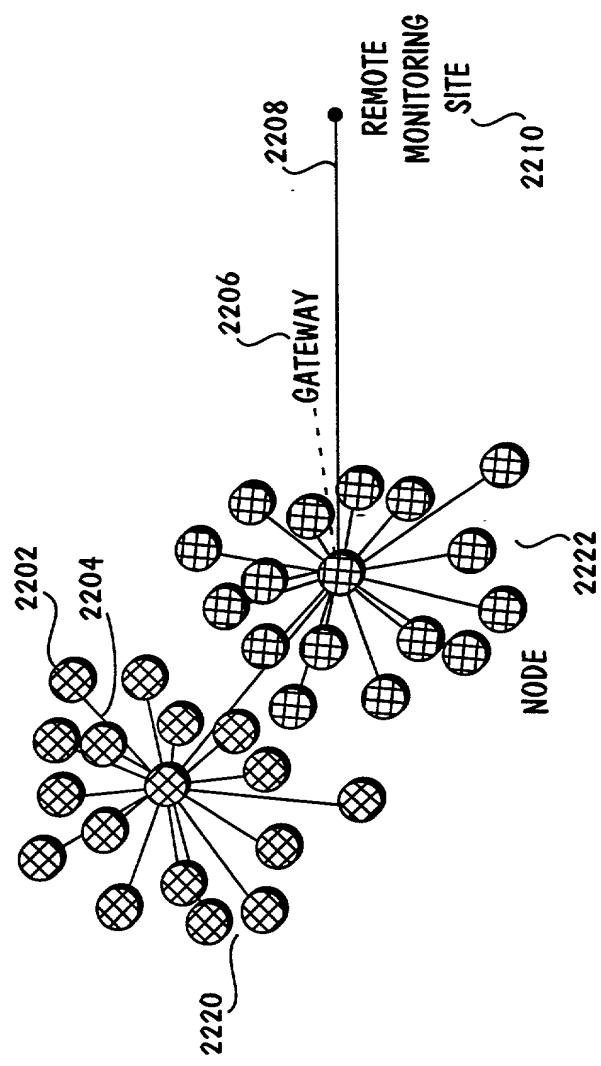


FIG. 22

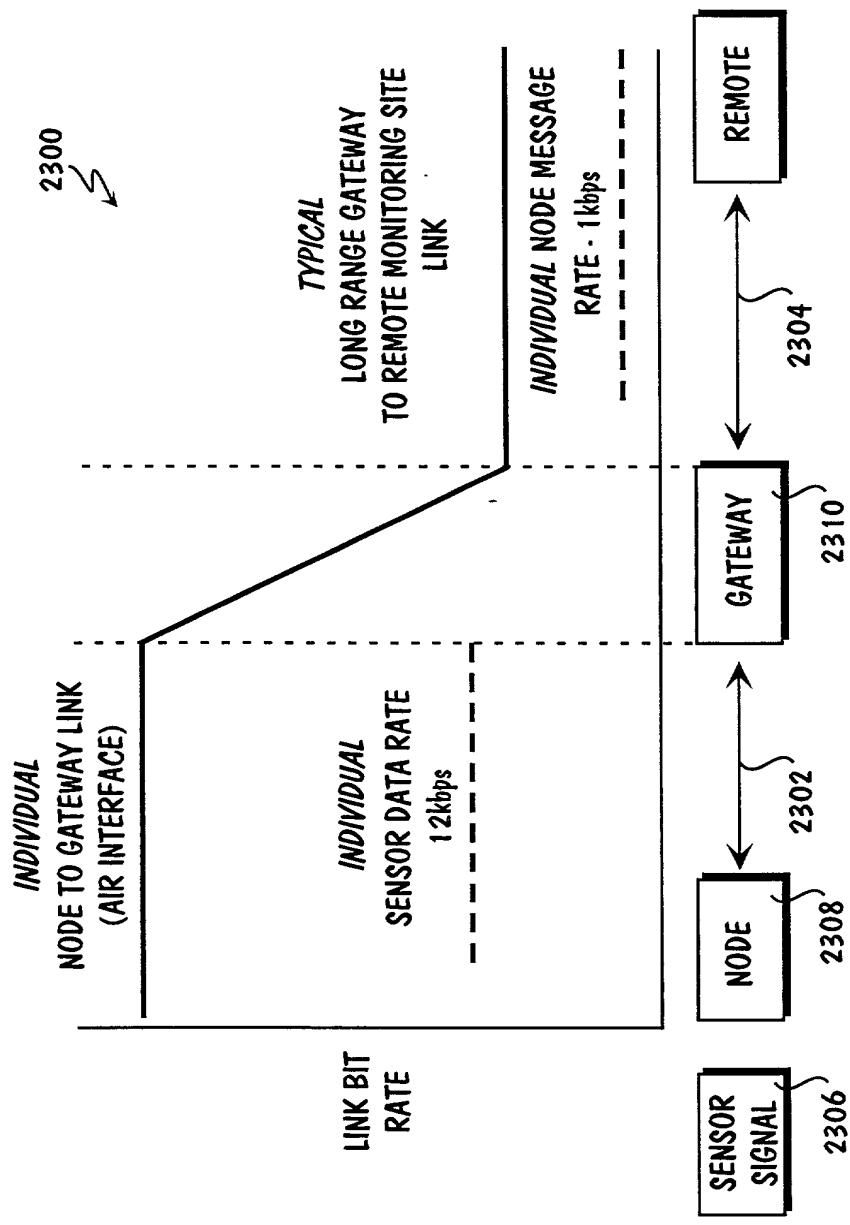


Fig. 23

Fig. 24

LENGTH (LOW ORDER BYTE)	LENGTH (HIGH ORDER BYTE)	DESTINATION ADDRESS	SOURCE ADDRESS	DATA (UP TO 2048 BYTES)	CHECKSUM
----------------------------	-----------------------------	------------------------	-------------------	----------------------------	----------

2400  
↙

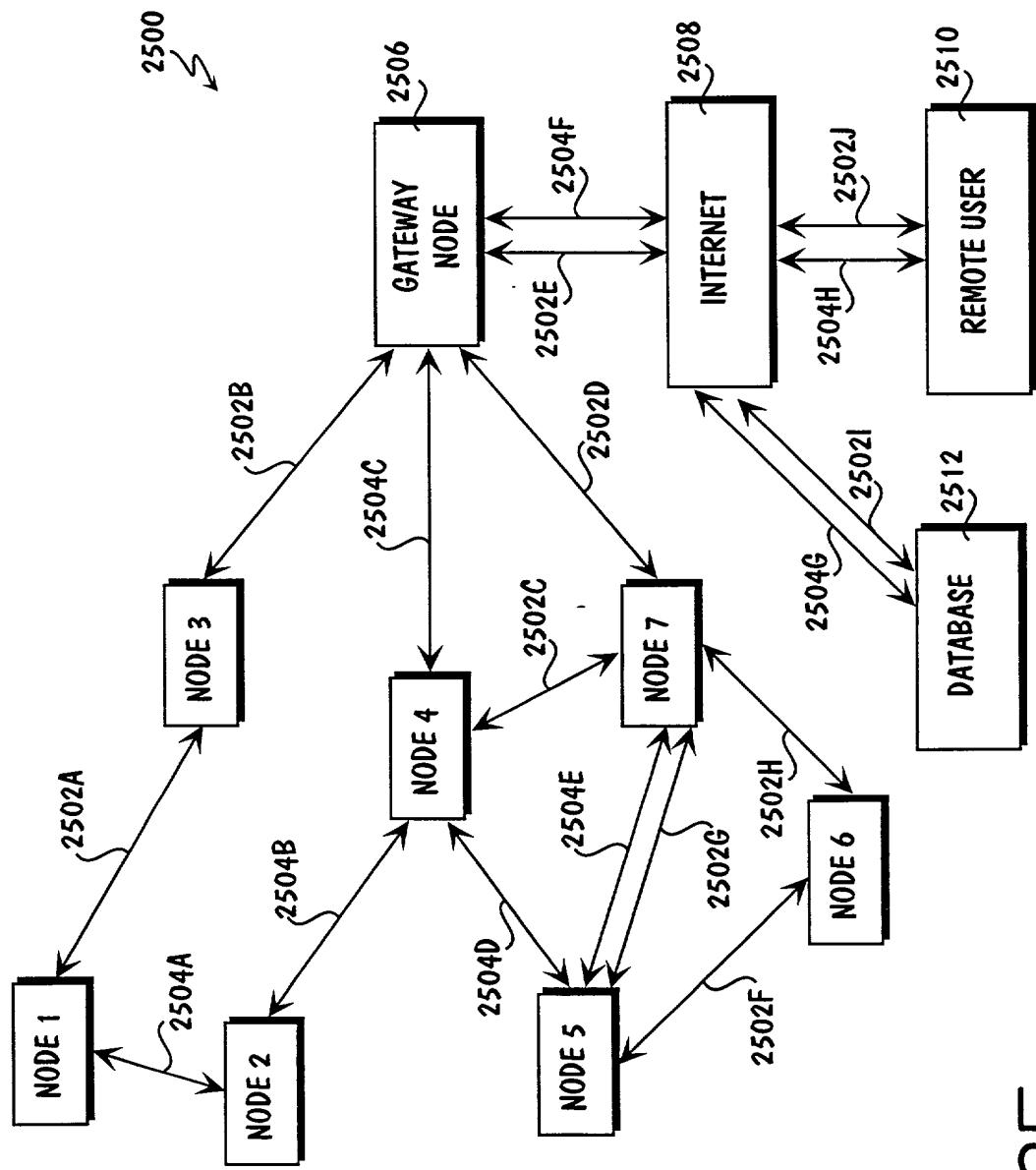


Fig. 25

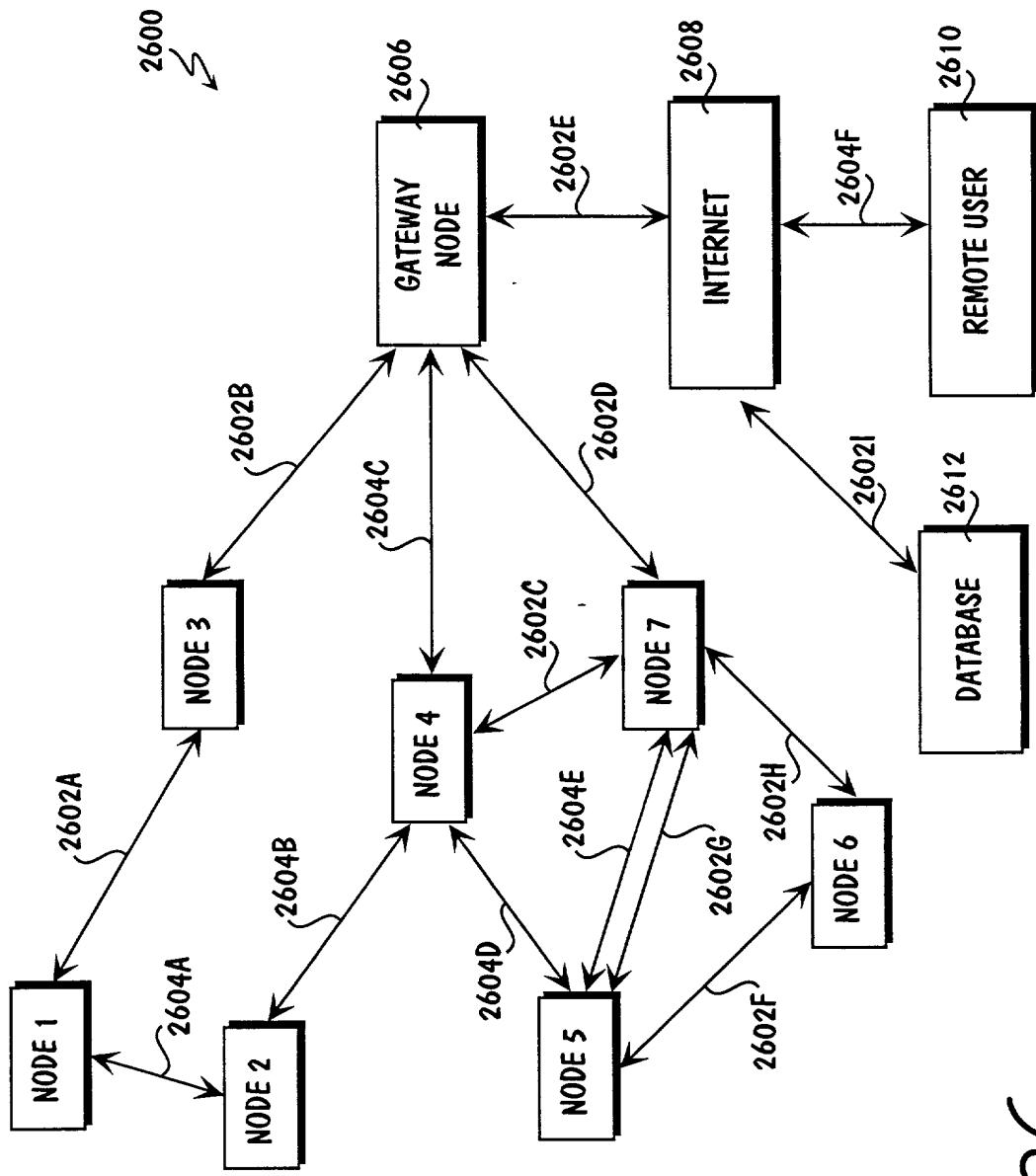
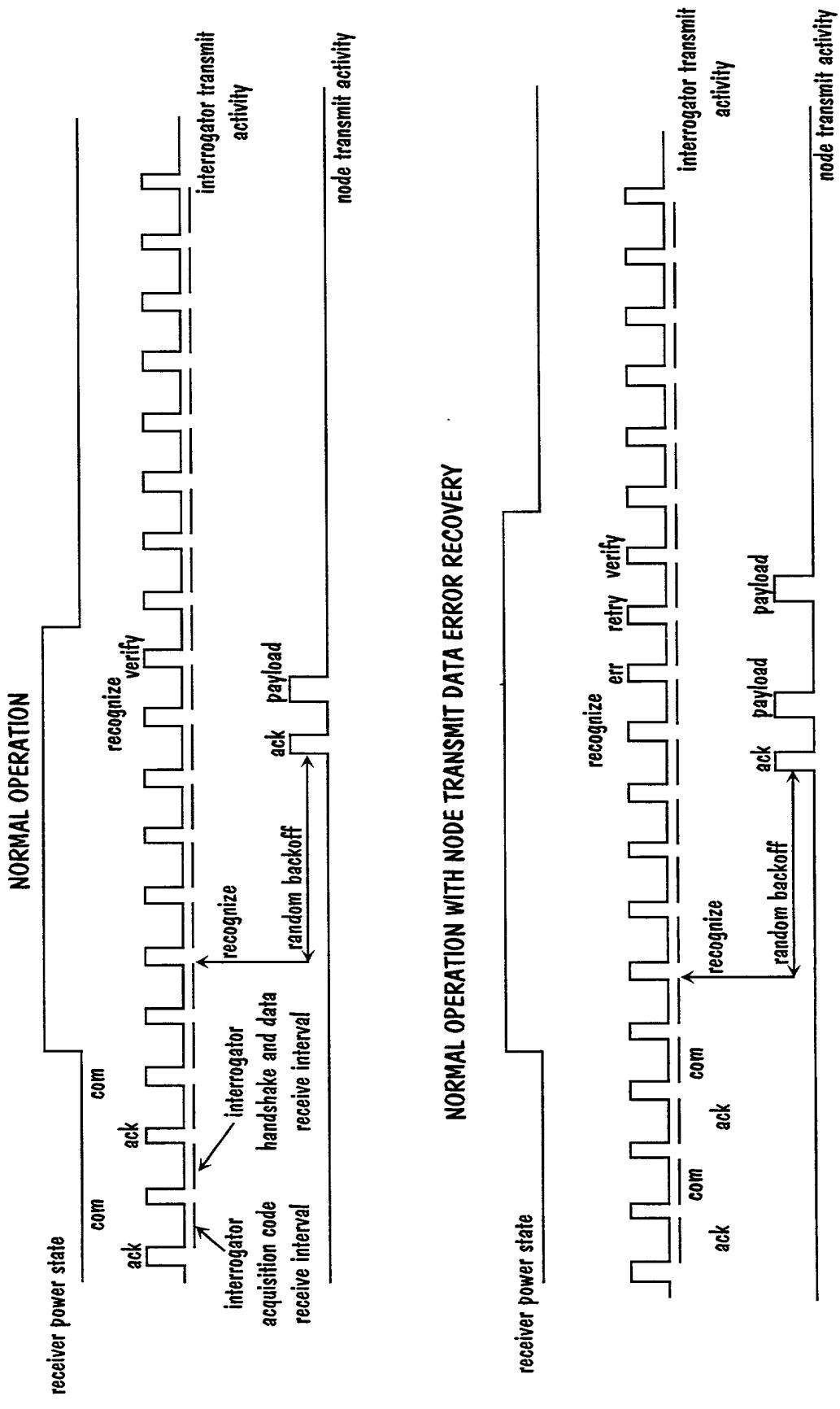
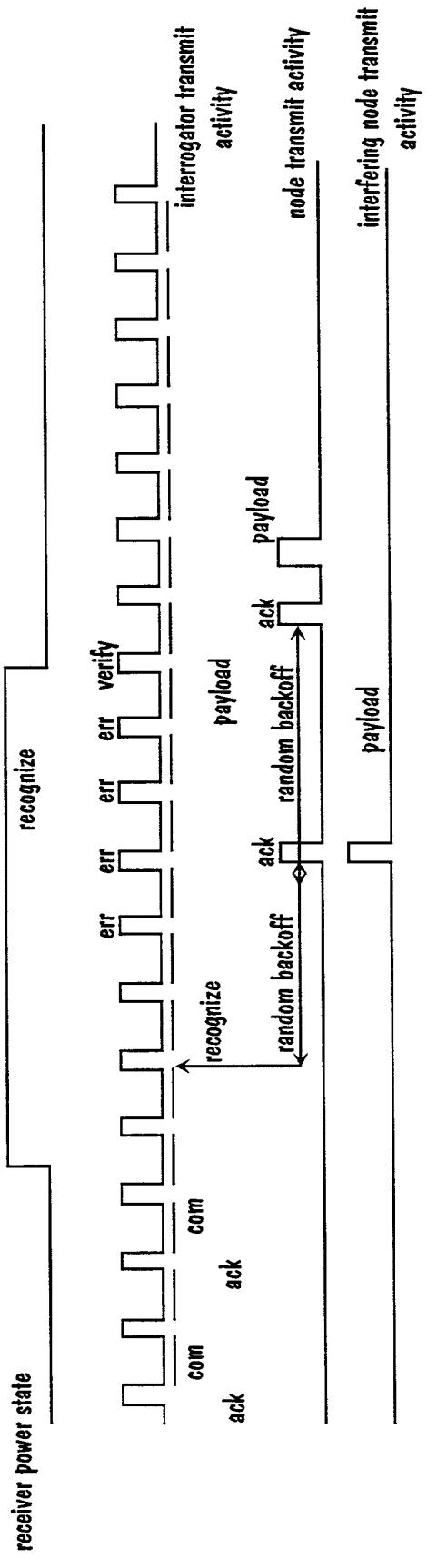


FIG. 26



### NORMAL OPERATION WITH ACQ COLLISION ERROR RECOVERY



### NORMAL OPERATION WITH ACQ CONFLICT RECOVERY

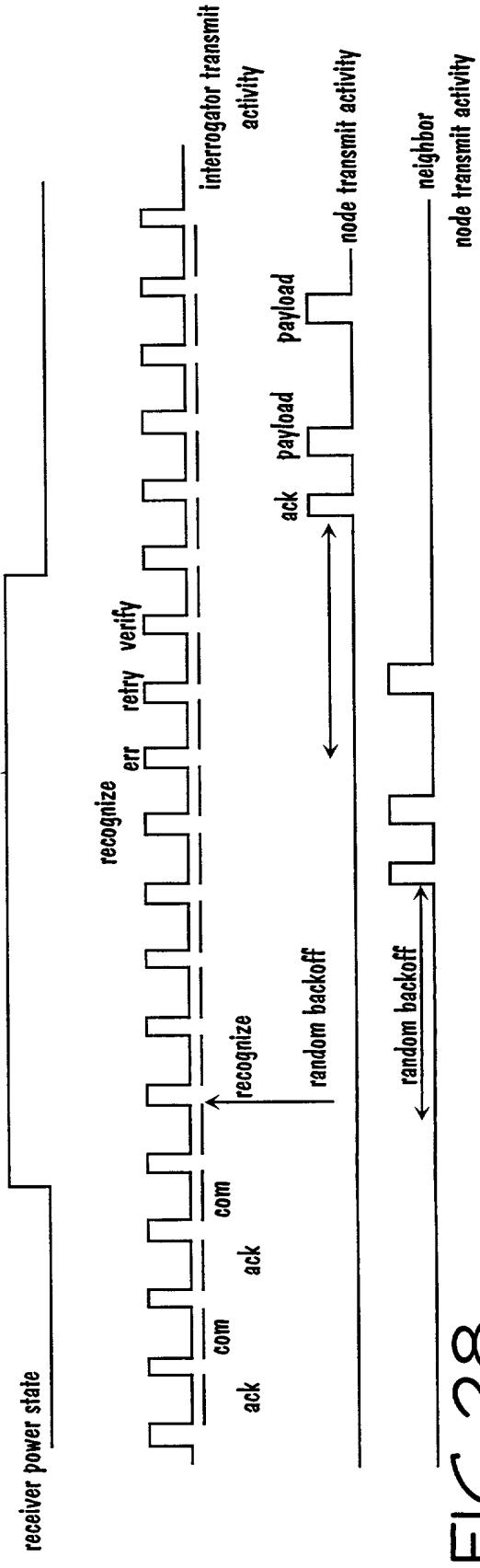


FIG. 28

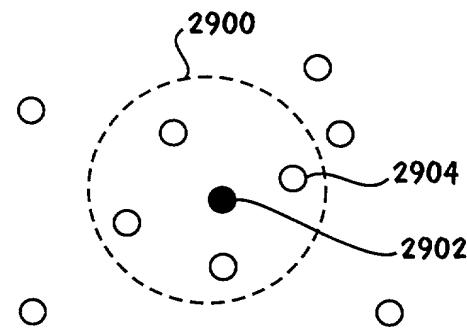


FIG. 29

1000 900 800 700 600 500 400 300 200 100 0

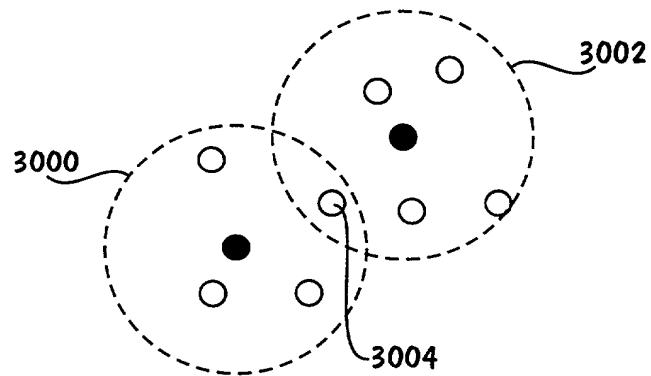


FIG. 30

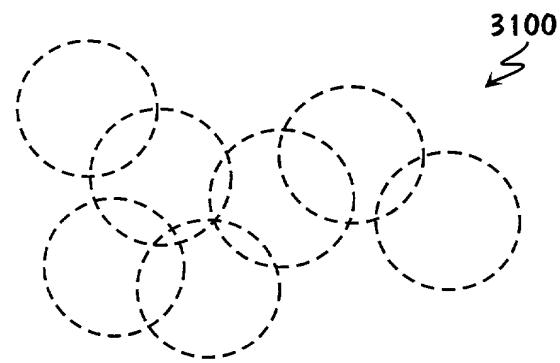


FIG. 31

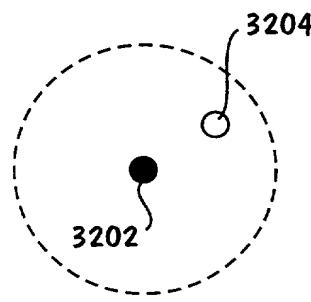


FIG. 32

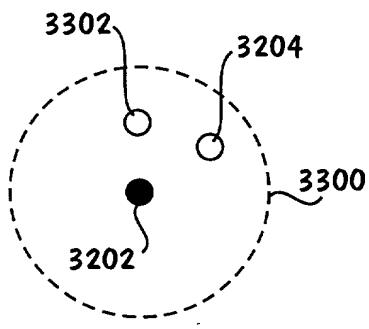


FIG. 33

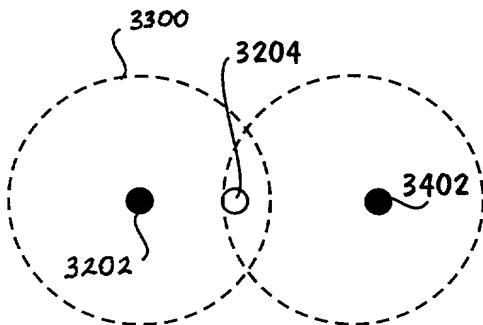


FIG. 34

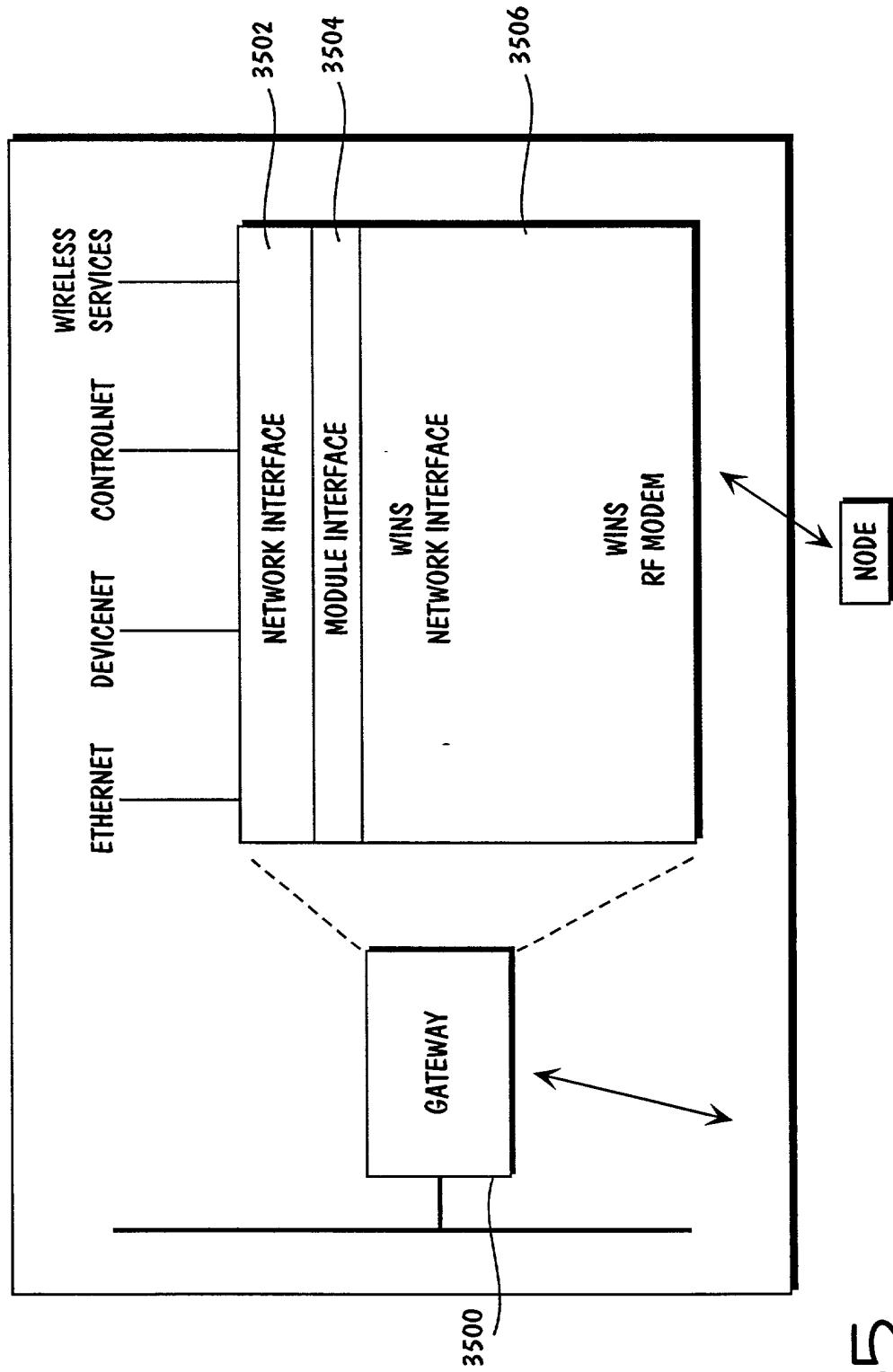


FIG. 35

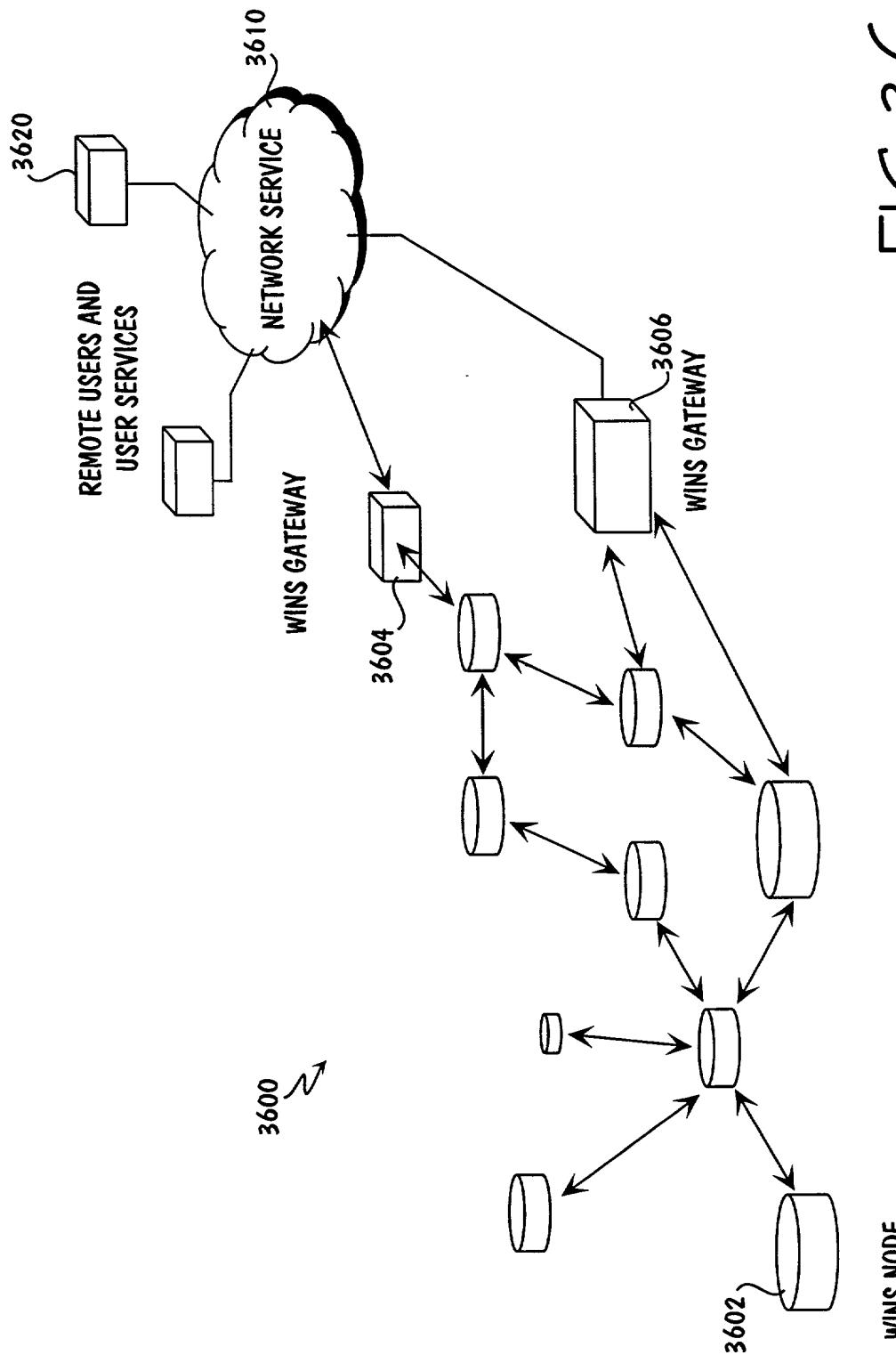


FIG. 36

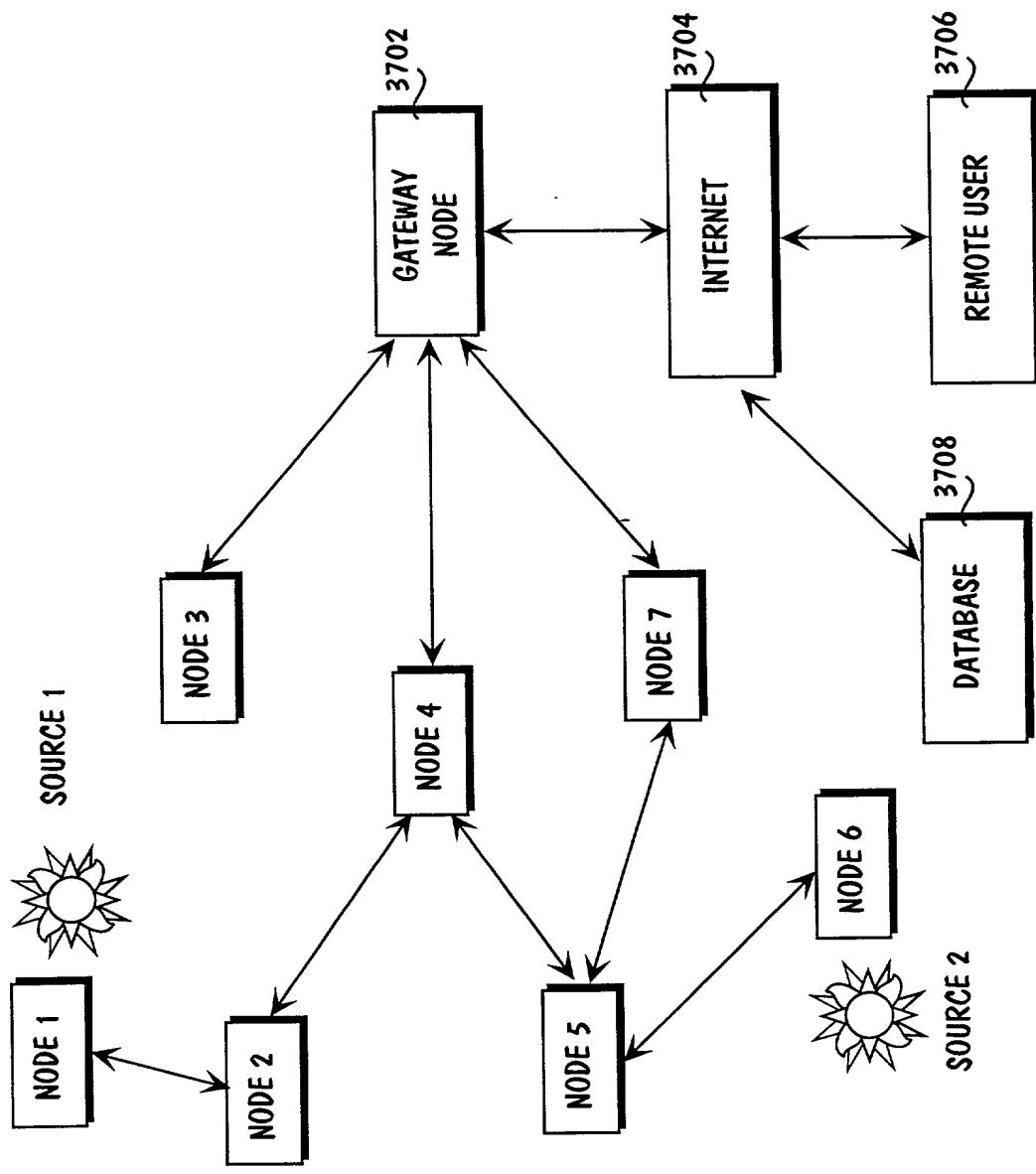


FIG. 37

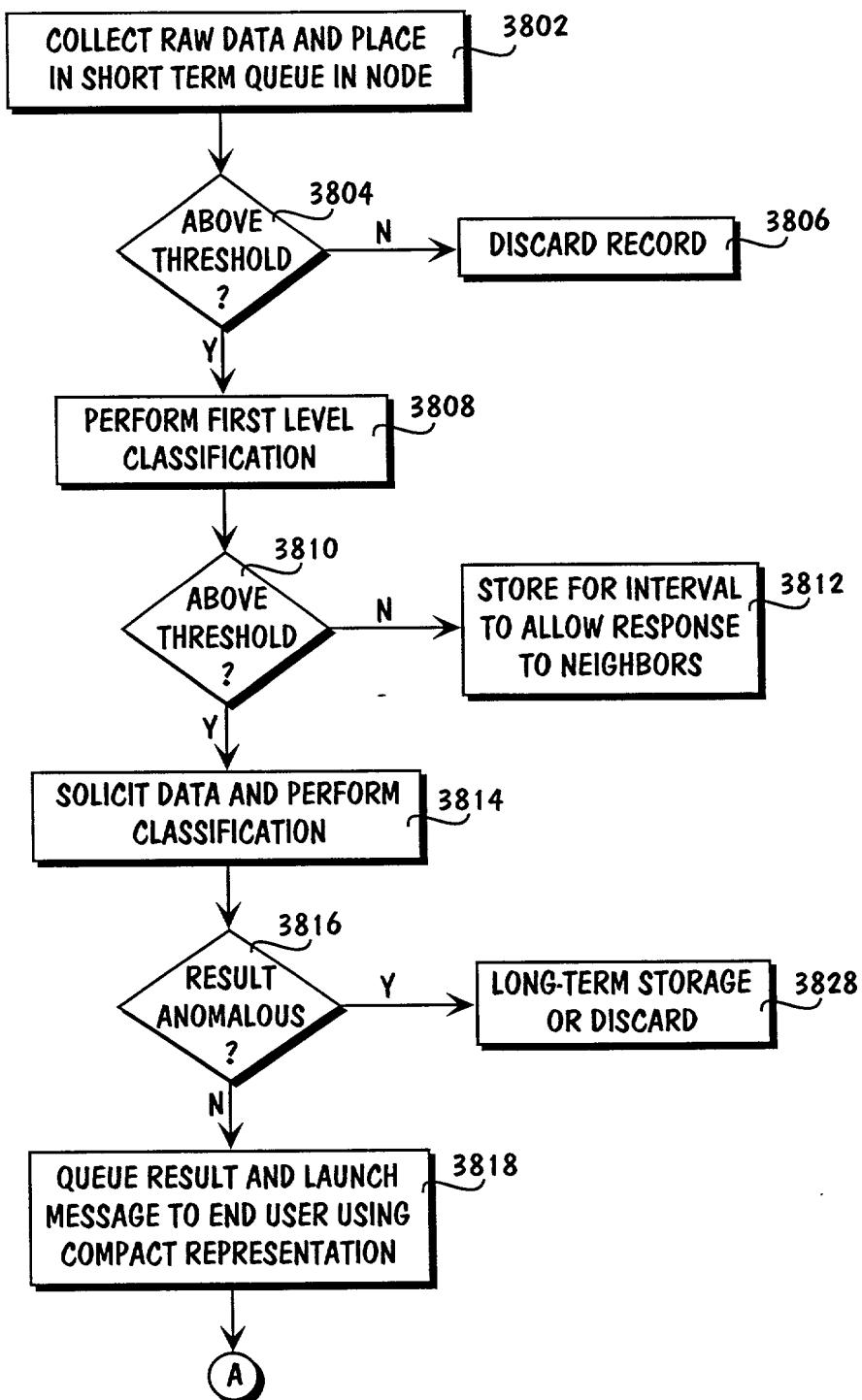


FIG. 38A

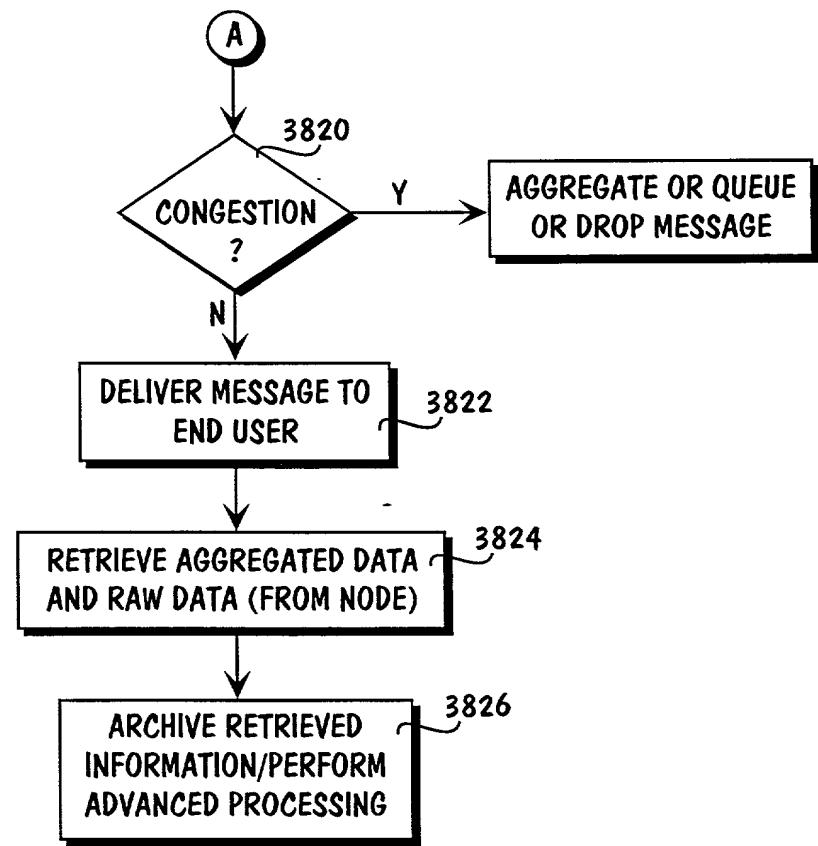
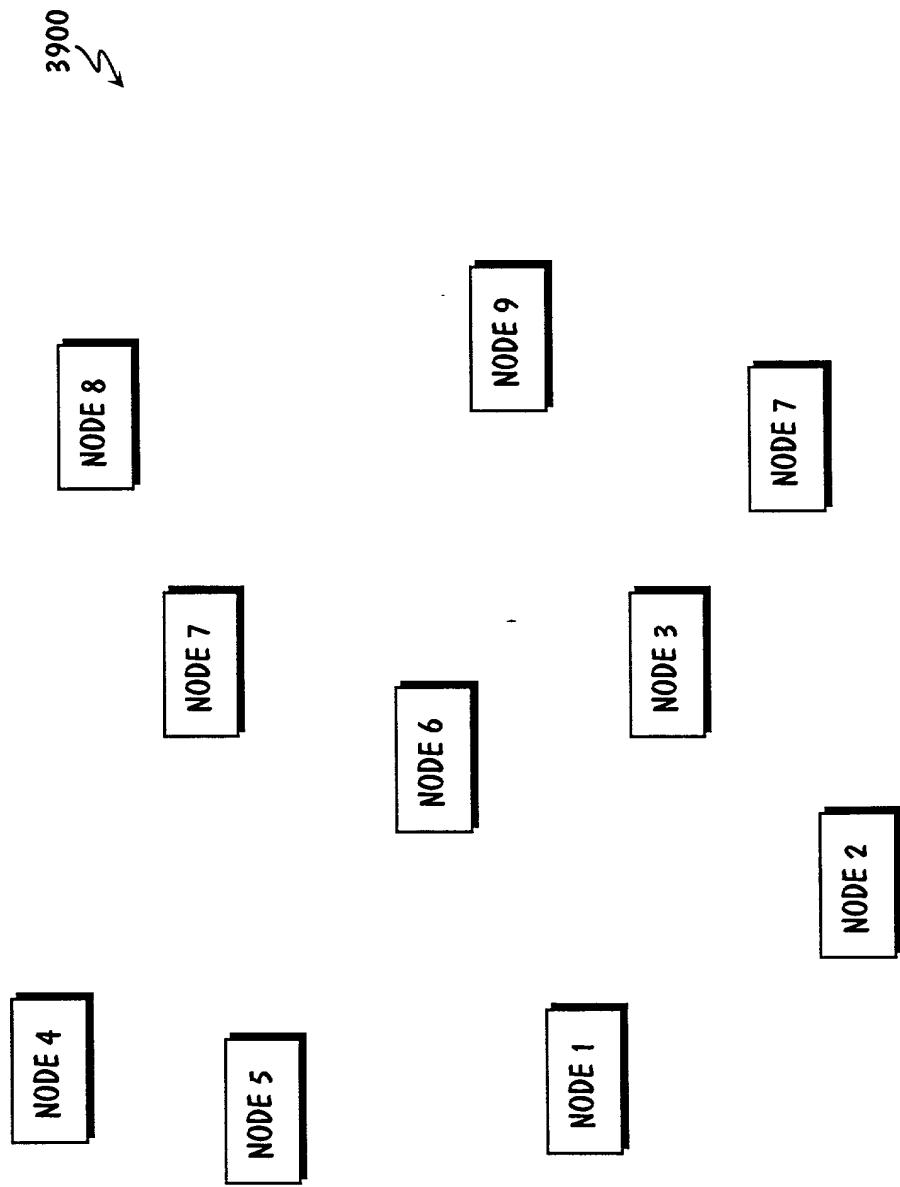


FIG. 38B

FIG. 39



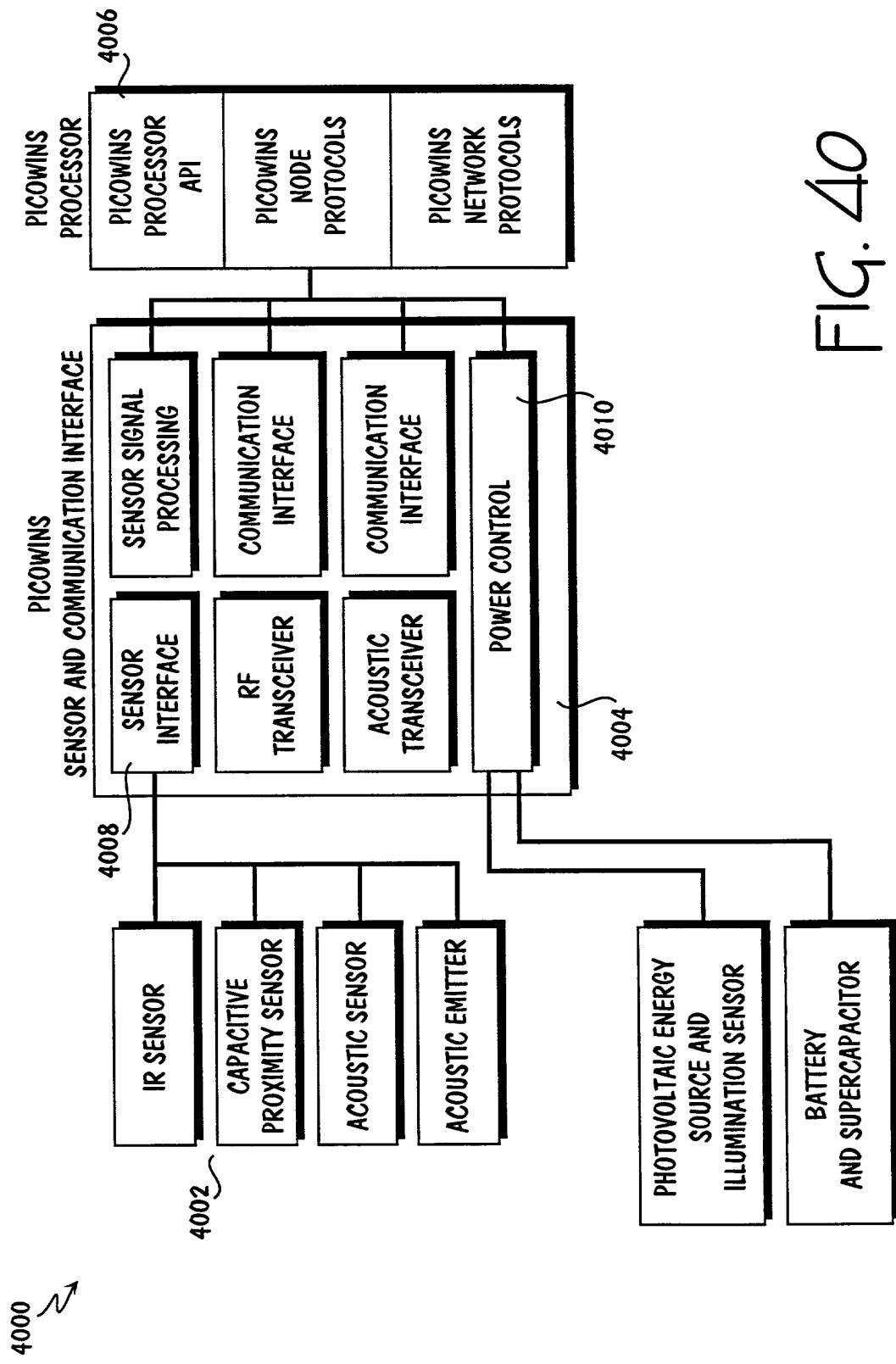
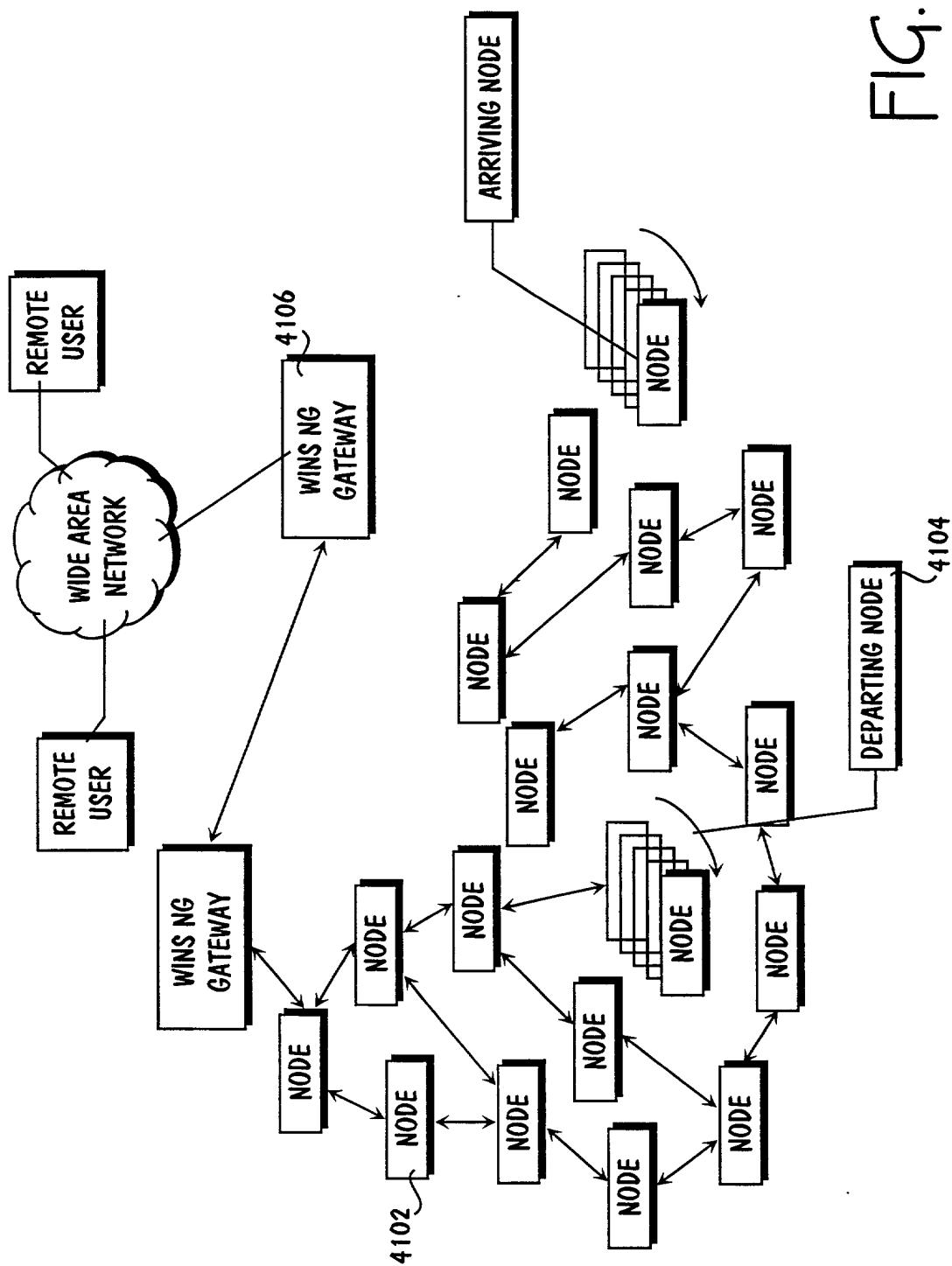


FIG. 40

Fig. 4



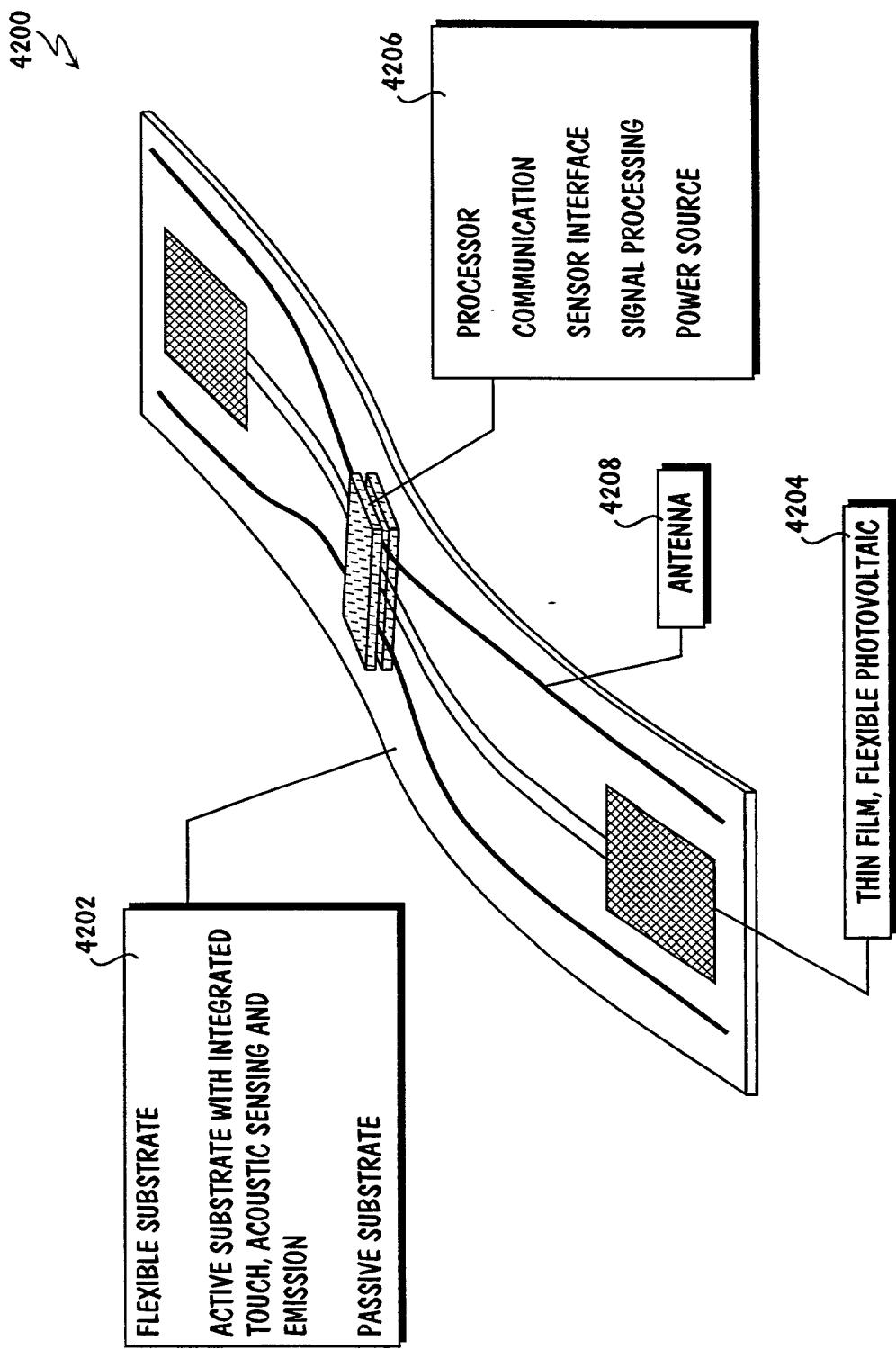


FIG. 42

Integrated sensors:  
3-axis MEMS  
accelerometer  
Inductive sensor  
acoustic, proximity,  
etc... **4310**

Processor: **4308**  
Communication  
Sensor Interface  
Signal Processing

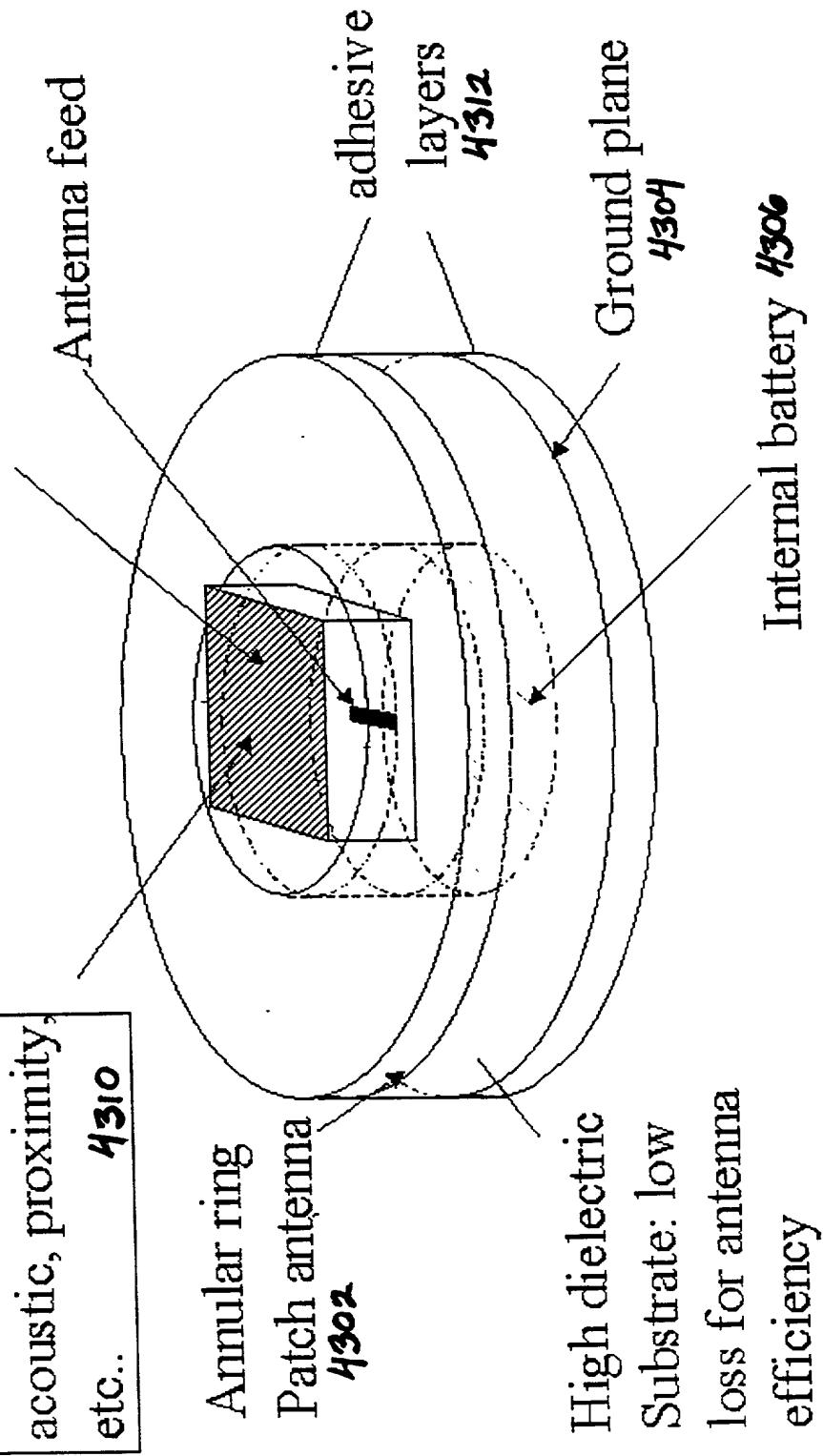
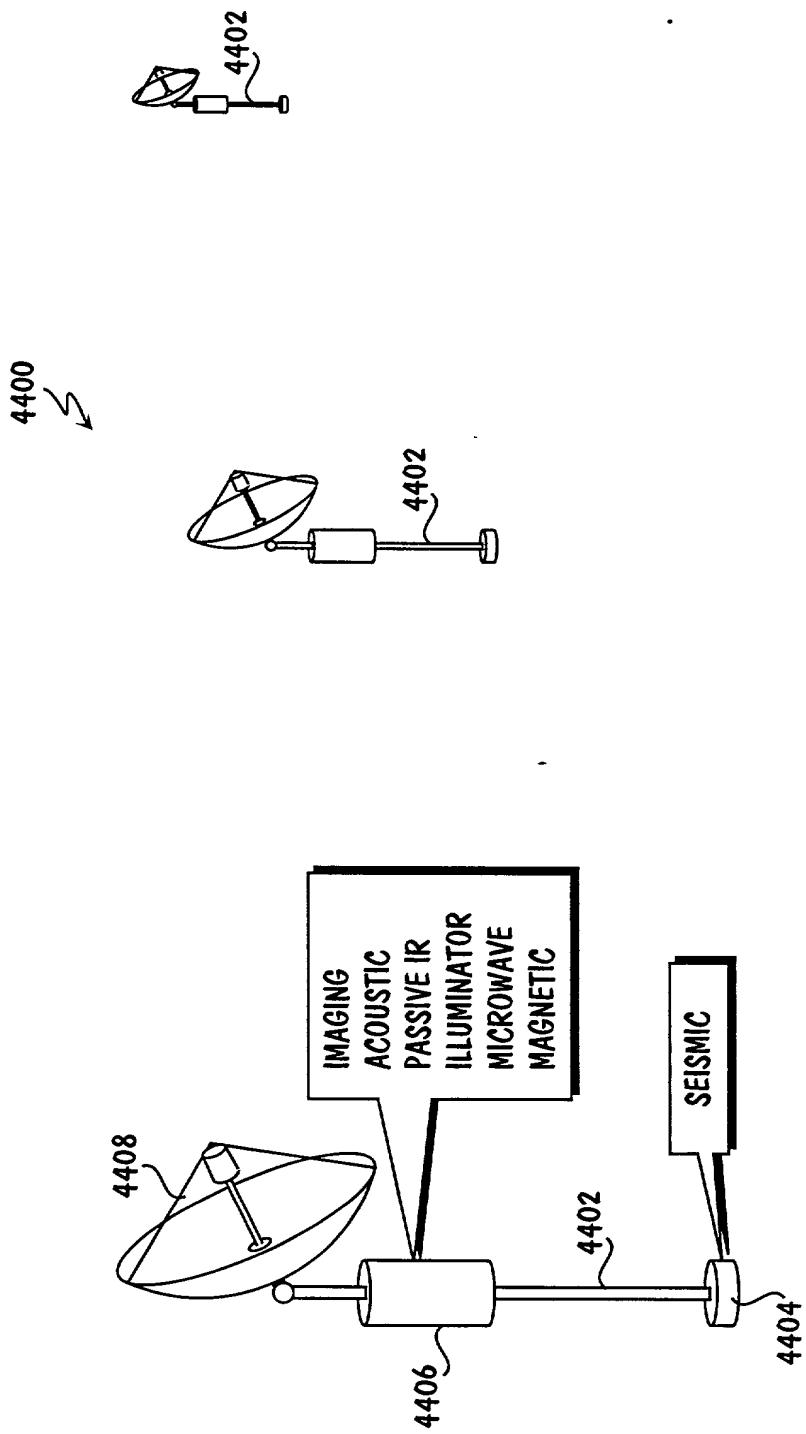


Figure 4/3

FIG. 44



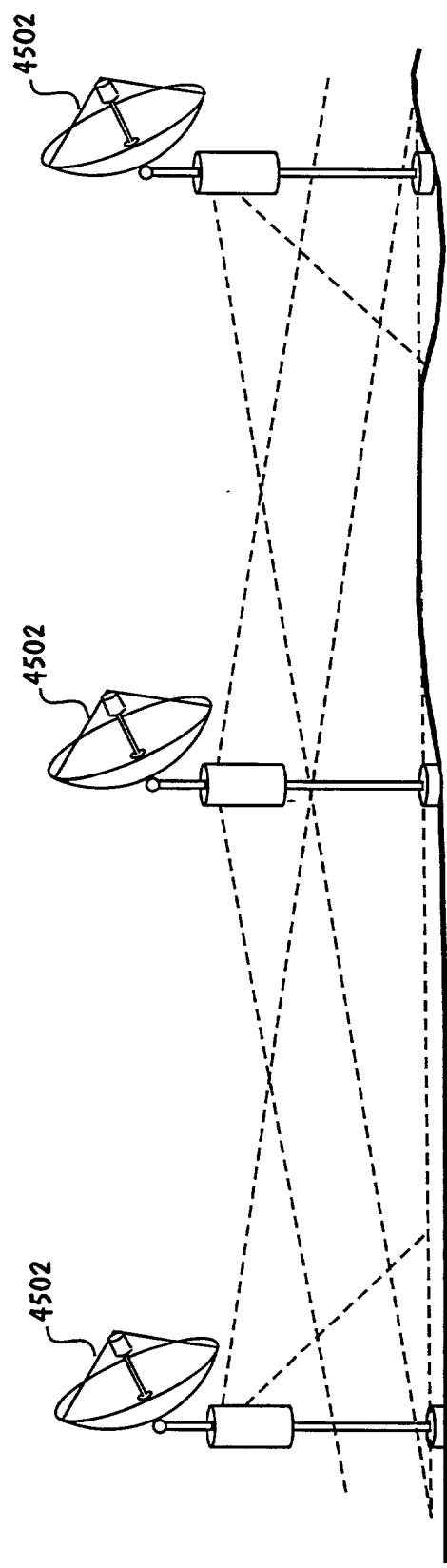


FIG. 45

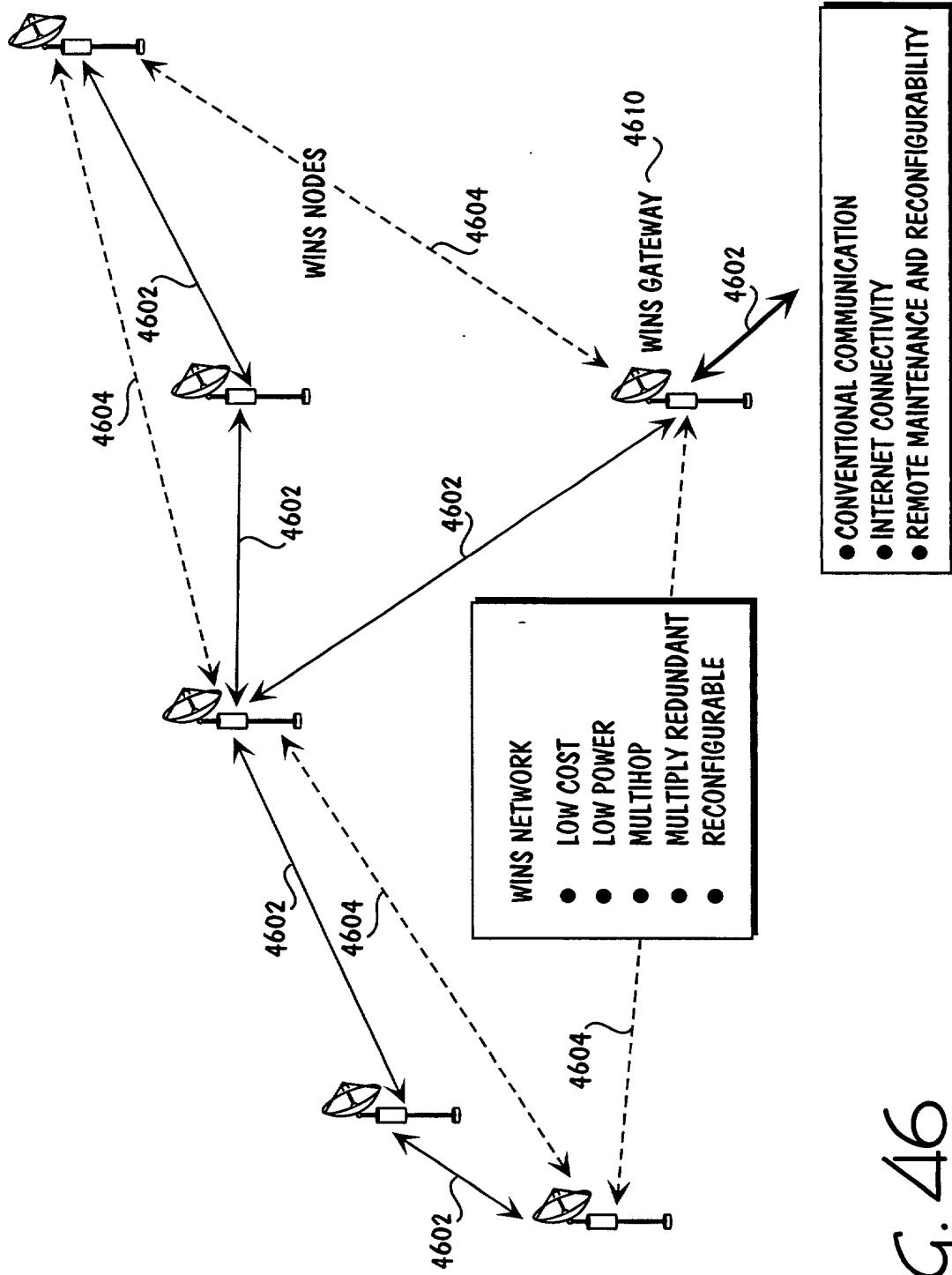


FIG. 46

FIG. 47

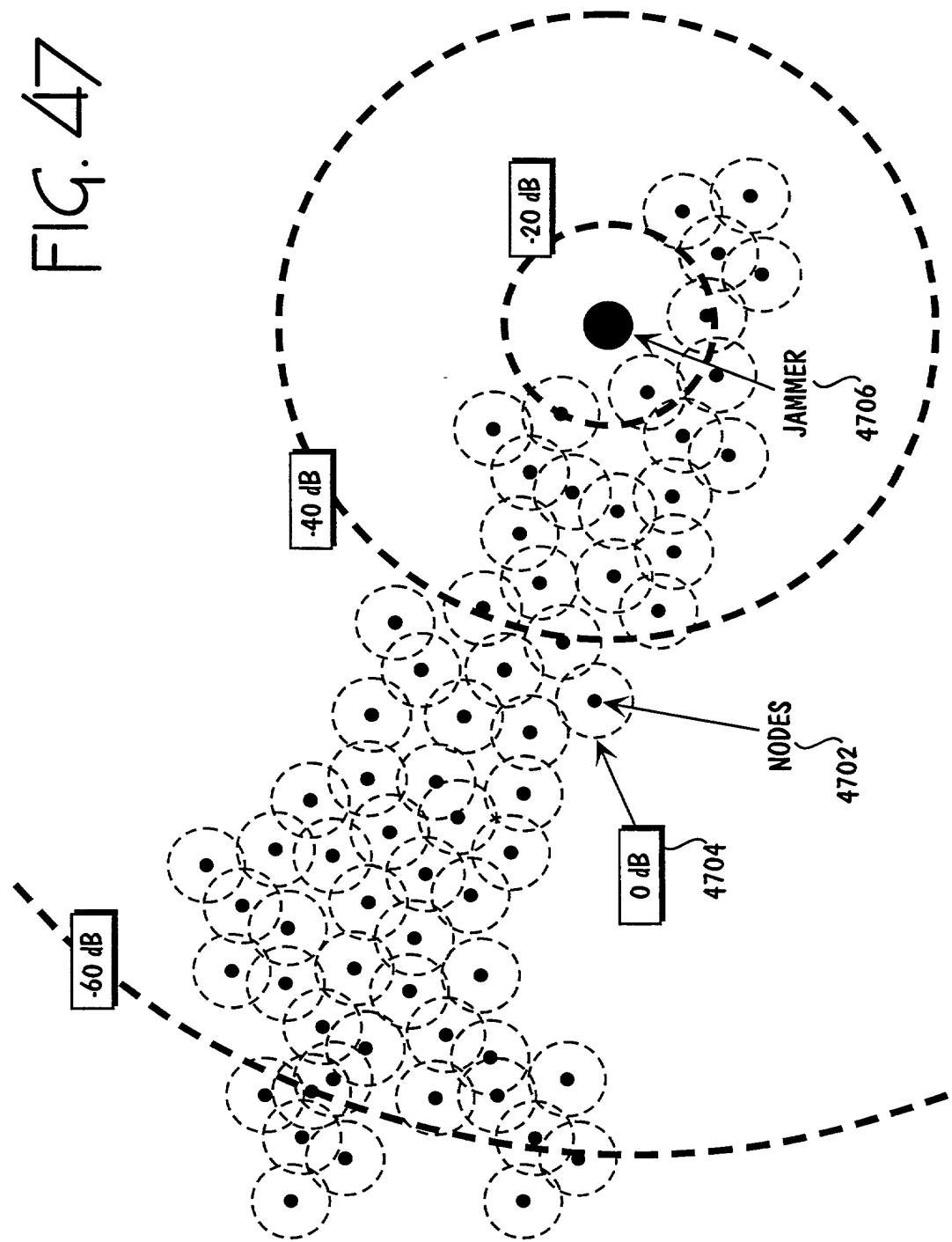


FIG. 48

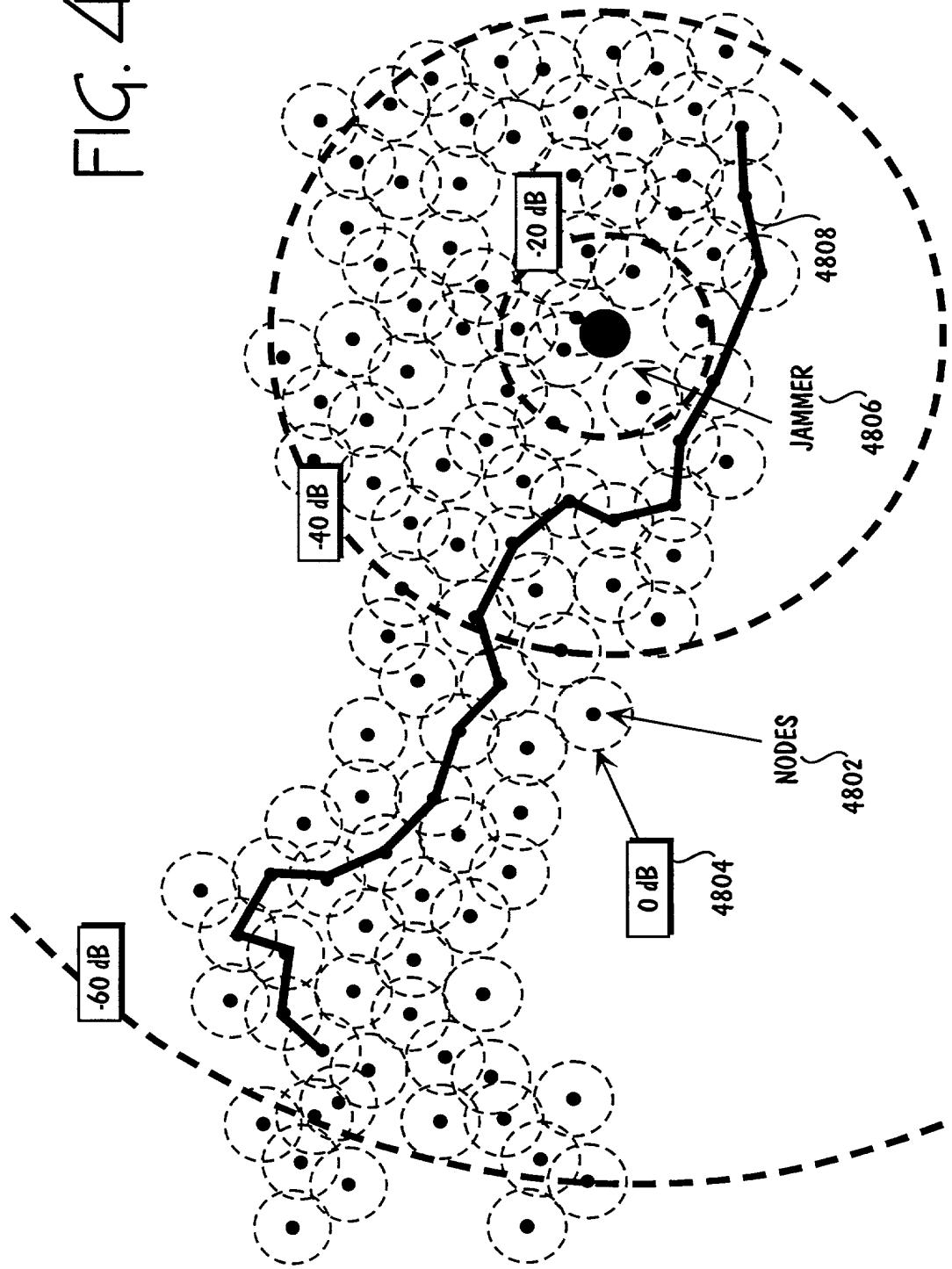
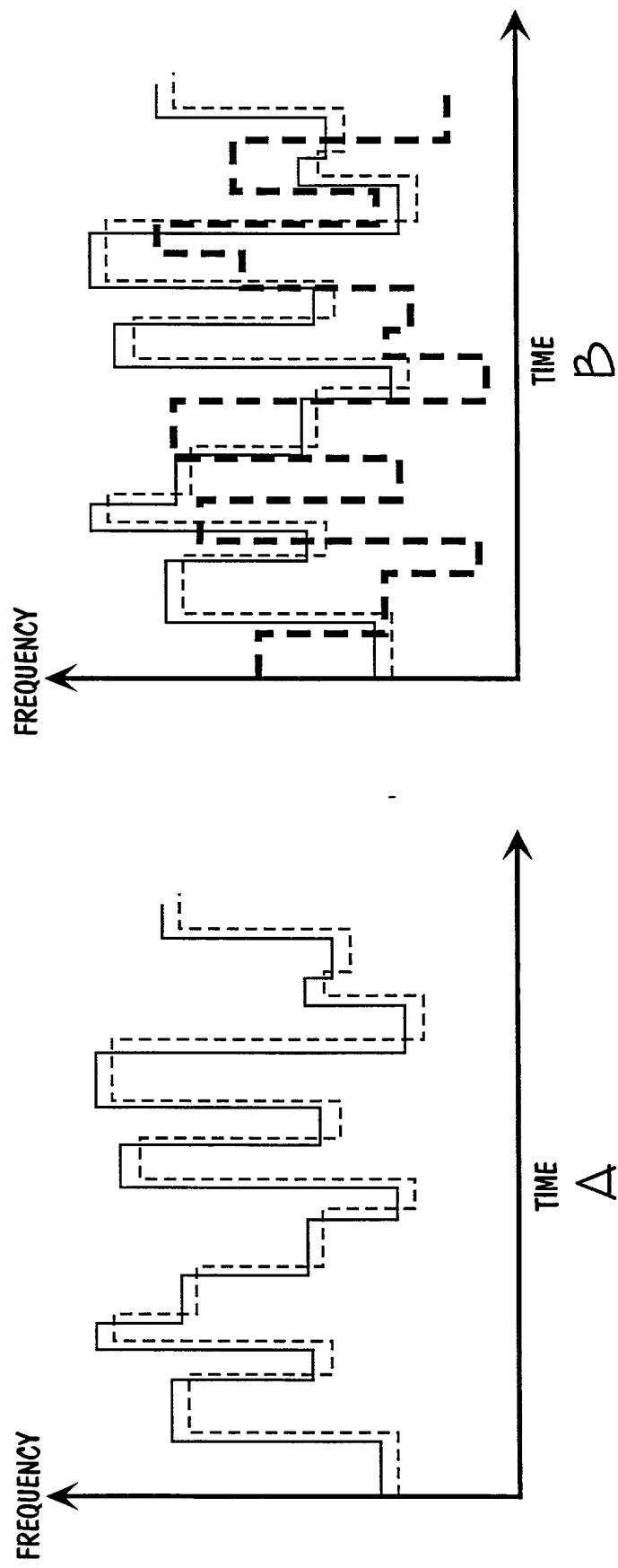


FIG. 49



WINS ASSET MANAGEMENT ARCHITECTURE

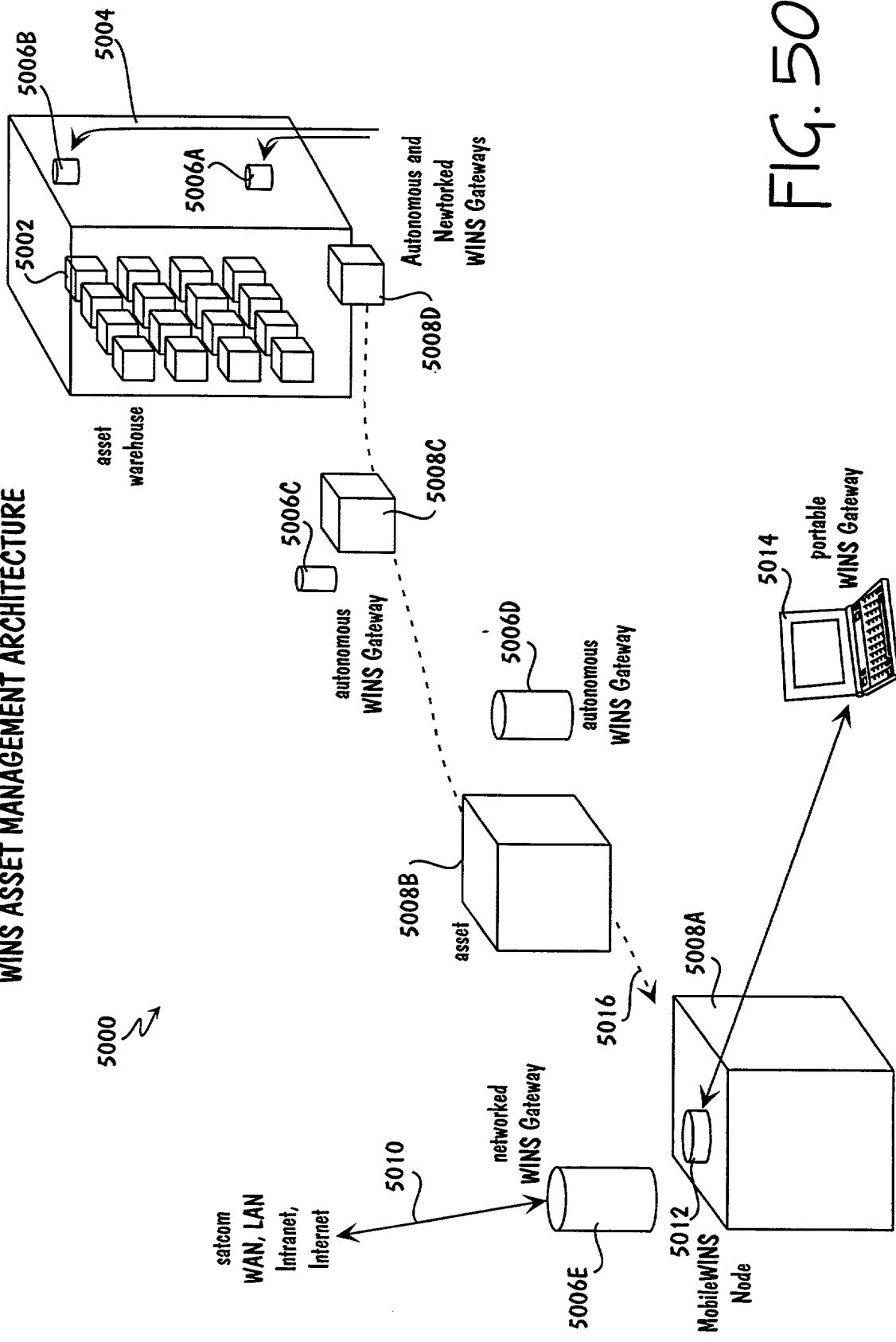


FIG. 50

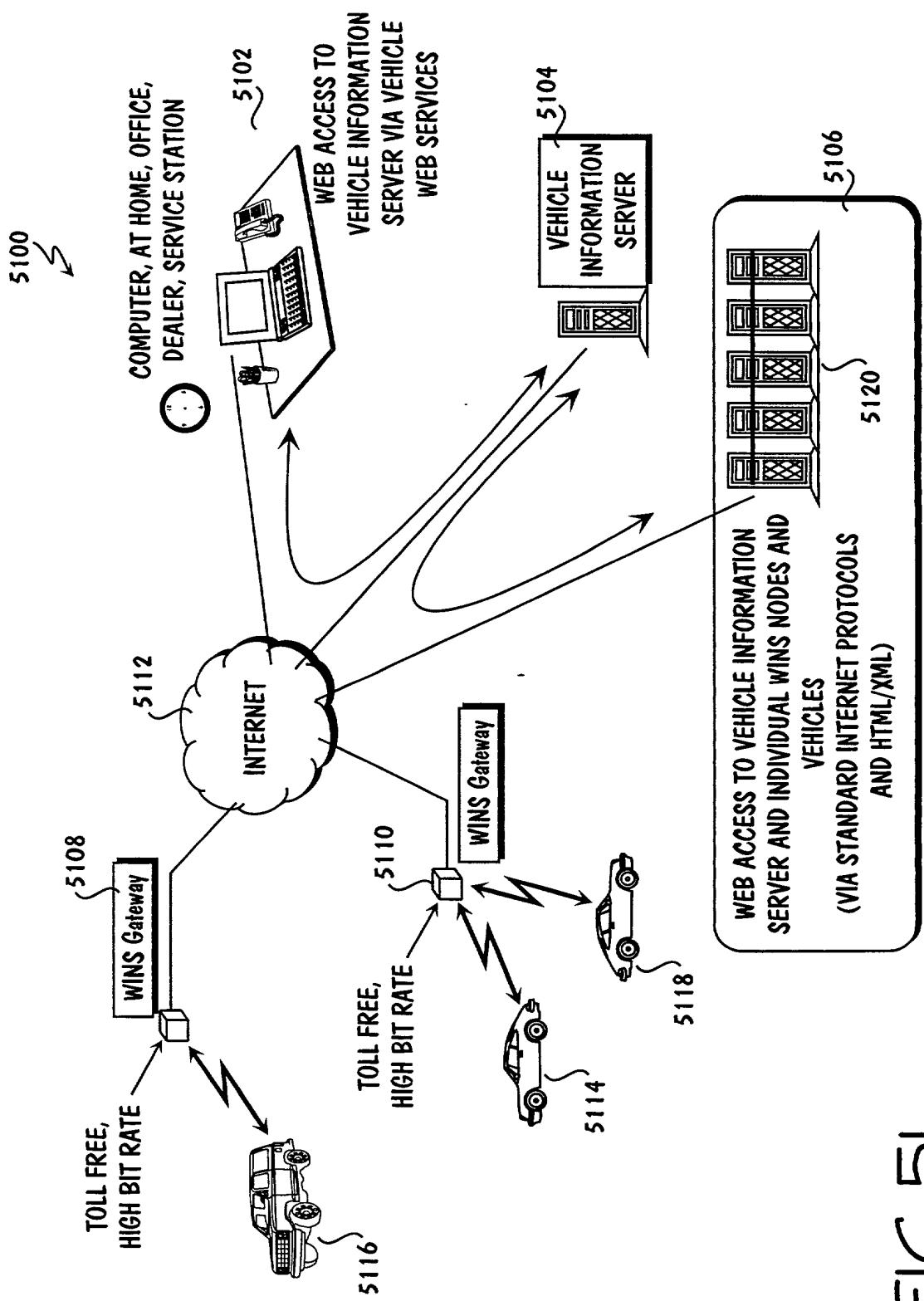


FIG. 51

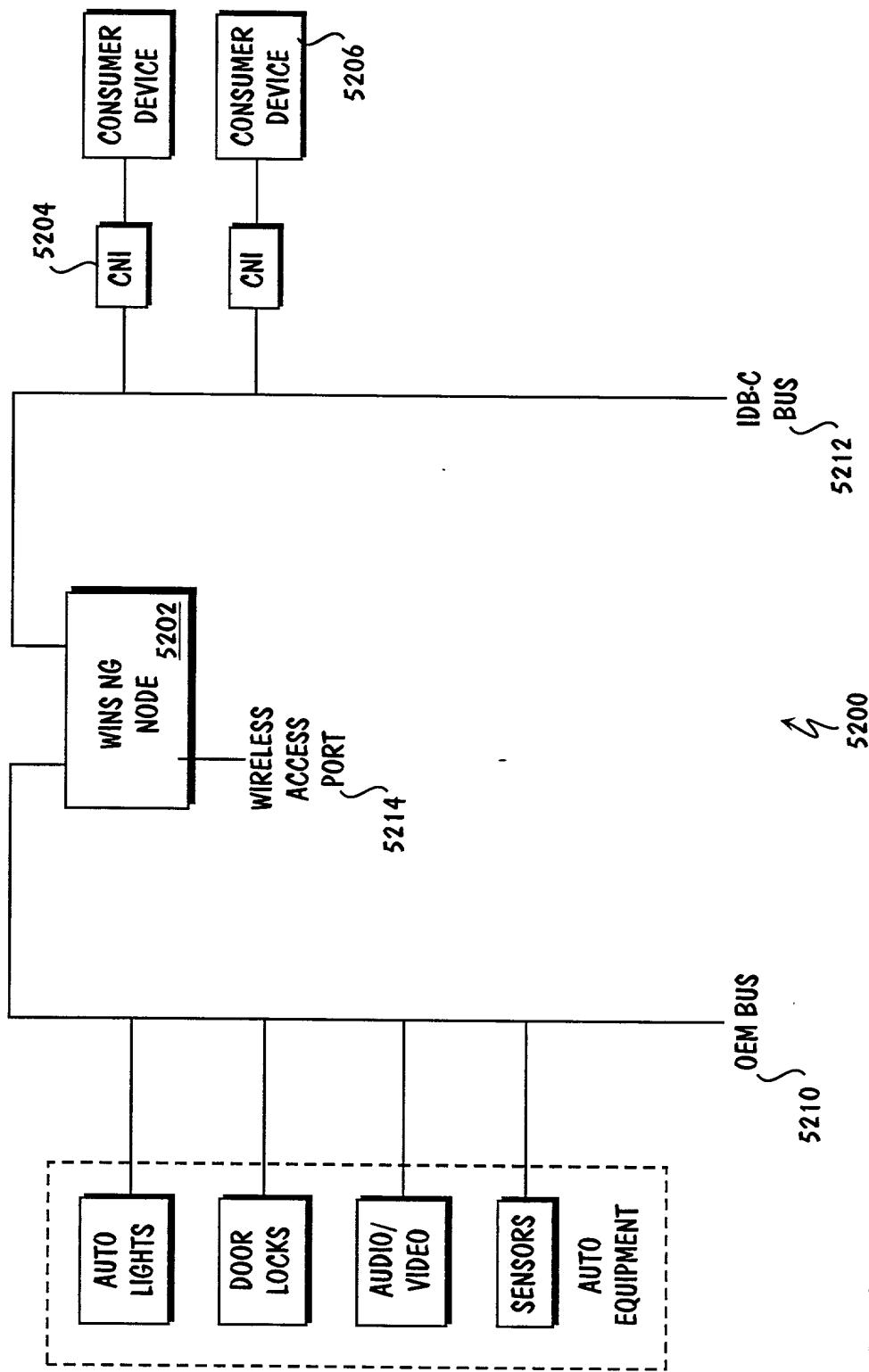


FIG. 52